## Device handbook Aplus-LED

Operating Instructions ApLus with LED display or without display 157 679-16

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## Warning notices

In this document warning notices are used, which you have to observe to ensure personal safety and to prevent damage to property. Depending on the degree of danger the following symbols are used:

If the warning notice is not followed death or severe personal injury will result.


If the warning notice is not followed damage to property or severe personal injury may result.

If the warning notice is not followed the device may be damaged or may not fulfill the expected functionality.

## Qualified personnel

The product described in this document may be handled by personnel only, which is qualified for the respective task. Qualified personnel have the training and experience to identify risks and potential hazards when working with the product. Qualified personnel are also able to understand and follow the given safety and warning notices.

## Intended use

The product described in this document may be used only for the application specified. The maximum electrical supply data and ambient conditions specified in the technical data section must be adhered. For the perfect and safe operation of the device proper transport and storage as well as professional assembly, installation, handling and maintenance are required.

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## 1. Introduction

### 1.1 Purpose of this document

This document describes the universal measurement device for heavy-current quantities APLus. It is intended to be used by:

- Installation personnel and commissioning engineers
- Service and maintenance personnel
- Planners


## Scope

This handbook is valid for all hardware versions of the APLUS with LED display or without display. Some of the functions described in this document are available only, if the necessary optional components are included in the device.

## Required knowledge

A general knowledge in the field of electrical engineering is required. For assembly and installation of the device knowledge of applicable national safety regulations and installation standard is required.

### 1.2 Scope of supply

- Measurement device Aplus
- Safety instructions (multiple languages)
- Software and documentation CD
- Connection set basic unit: Plug-in terminals and mounting clamps
- Optional: Connection set I/O extension: Plug-in terminals


### 1.3 Further documents

On the CD supplied with the device the following documents about the ApLus are provided:

- Safety instructions Aplus
- Data sheet Aplus
- Modbus basics: General description of the communication protocol
- Modbus interface Aplus: Register description of Modbus/RTU communication via RS-485
- Modbus/TCP interface Aplus: Register description of Modbus/TCP communication via Ethernet


## 2. Security notes



The installation and commissioning should only be carried out by trained personnel.
Check the following points before commissioning:

- that the maximum values for all the connections are not exceeded, see "Technical data" section,
- that the connection wires are not damaged, and that they are not live during wiring,
- that the power flow direction and the phase rotation are correct.

The instrument must be taken out of service if safe operation is no longer possible (e.g. visible damage). In this case, all the connections must be switched off. The instrument must be returned to the factory or to an authorized service dealer.

It is forbidden to open the housing and to make modifications to the instrument. The instrument is not equipped with an integrated circuit breaker. During installation check that a labeled switch is installed and that it can easily be reached by the operators.

Unauthorized repair or alteration of the unit invalidates the warranty.

## 3. Device overview

### 3.1 Brief description

The Aplus is a comprehensive instrument for the universal measurement, monitoring and power quality analysis in power systems. The device can be adapted fast and easily to the measurement task by means of the supplied CB-Manager software. The universal measurement system of the device may be used directly for any power system, from single phase up to 4 -wire unbalanced networks, without hardware modifications. Independent of measurement task and outer influences always the same high performance is achieved.

Using additional, optional components the opportunities of the Aplus may be extended. You may choose from I/O extensions, communication interfaces or data logger. The nameplate on the device gives further details about the present version.

The version with top-hat rail adapter instead of the display has the same dimensions and connections as the version with display and supports the same options.

### 3.2 Possible modes of operation

The Aplus can cover a wide range of possible input ranges without any hardware variance. The adaption to the input signal is performed by means of variable amplifying levels for current and voltage inputs. Depending on the application it makes sense to fix these levels by means of the configuration or to let them stay variable to achieve a maximum accuracy during measurement. The differentiation, if the amplifying remains constant or is adapted to the present value, is done during the definition of the input configuration by means of the parameter "auto-scaling".

The disadvantage of auto-scaling is that when an amplifying level needs to be changed, a settling time of at least one cycle of the power frequency must be allowed until the signals have stabilized again. During this short time the measurement results remain frozen.

## Continuous measurement

An absolute uninterrupted measurement of all quantities assumes that auto-scaling is deactivated for both voltage and current inputs.

## Metering

The uncertainty of the active energy meters of the APLus is given with class 0.5 S . To fulfill the high requirements of the underlying meter standard EN 62053-22 also small currents have to be measured very accurate. To do so, auto-scaling must be activated for current inputs. For metering applications the system voltage is assumed to be quite constant, nominal value acc. standard, wherefore auto-scaling for voltages is not required. The subsequent example shows an appropriate configuration, which also conforms to the factory setting of the device.


## Dynamic monitoring of limit values

An important criterion when monitoring the quality of the supply voltage is the possibility to detect short sags of the system voltage. To be able to follow the progress of the voltage auto-scaling of the voltage inputs should be deactivated. Thereby you have to consider that a possible swell of the voltage may be detected only up to the configured overriding ( $20 \%$ of rated voltage in the above example), because the switching of the measurement range is locked in both directions.
This applies analogously to all quantities of the system, whose progress should be monitored. For power quantities the voltage amplification as well as the current amplification is influenced. However, which basic quantities may vary how much can differ from application to application.

### 3.3 Monitoring and alarming

The logic module integrated in the ApLus is a powerful feature to monitor critical situations without delay on device side. By implementing this local intelligence a safe monitoring can be realized which is independent of the readiness of the control system.

### 3.3.1 Alarming concept

How alarms are handled is decided during the configuration of the device. For that in the logic module you can define if LED's are used for alarm state display and how resp. when a possibly activated action, such as the switching of a relay, will be reset. These configuration parameters are highlighted in yellow in the following chart.


Acknowledgment: This procedure affects the state of the LED only
If an alarm state is visualized via LED, its occurence must be acknowledged via display (see:
Acknowledgment of alarms via display), no matter if it is still active (fast flashing) or has dropped-out already (slow flashing). By acknowledging an alarm, only the flashing of the LED stops, but a reset of the alarm action is performed only if the display is configured as a possible source for alarm reset.

Acknowledgment is not required if "acknowledgement of alarm LEDs required" in the logic module configuration is not selected.

Alarm reset: This procedure affects the states of the follow-up action and the LEDs
If an alarm state occurs a follow-up action (e.g. the switching of a relay) can be triggered. This follow-up action is normally reset as soon as the alarm condition no longer exists. But the alarm handling may be configured as well in a way that only by means of an alarm reset the subsequent operation is withdrawn. This way an alarm remains stored until a reset is performed, even if the alarm situation no longer exists. Possible sources for an alarm reset are the display, a digital input, another logical state of the logic module or a command via the bus interface.
Hint: If an alarm is reset, the alarm state visualized via LED is acknowledged at the same time.

On the next page some signal flow examples are shown.


Z: Logic output determined from all involved logic inputs
D: Corresponds to signal $Z$, delayed by the switch-in resp. dropout delay
A: Output signal of the logic function
S: State of the subsequent operation (e.g. of a relay), corresponds normally to A, but may be inverted (subsequent operation: relay OFF)

1) Alarm reset inactive, switch-in and dropout delay 3s, follow-up action not inverted

2) Alarm reset active, switch-in and dropout delay 0s, follow-up action inverted


### 3.3.2 Logic components

The logic outputs are calculated via a two level logical combination of states, which are present at the inputs. Usable components are AND, OR and XOR gates as well as their inversions NAND, NOR and XNOR.


The principal function of the logical gates is given in the following table, for simplicity shown for gates with two inputs only.

|  | symbol | older symbols |  | truth table |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| function |  | ANSI 91-1984 | DIN 40700 (alt) |  |  |  | plain text |
| AND |  | $B-Y$ | $B \longrightarrow$ | A | B | Y | Function is true if all input conditions are fulfilled |
|  |  |  |  | 0 | 0 | 0 |  |
|  |  |  |  | 0 | 1 | 0 |  |
|  |  |  |  | 1 | 0 | 0 |  |
|  |  |  |  | 1 | 1 | 1 |  |
| NAND | $B-\&-Y$ | $\mathrm{P}-\mathrm{P}$ |  | A | B | Y | Function is true if at least one of the input conditions is not fulfilled |
|  |  |  |  | 0 | 0 | 1 |  |
|  |  |  |  | 0 | 1 | 1 |  |
|  |  |  |  | 1 | 0 | 1 |  |
|  |  |  |  | 1 | 1 | 0 |  |
| OR |  |  |  | A | B | Y | Function is true if at least one of the input conditions is fulfilled |
|  |  |  |  | 0 | 0 | 0 |  |
|  |  |  |  | 0 | 1 | 1 |  |
|  |  |  |  | 1 | 0 | 1 |  |
| NOR |  |  |  | A | B | Y | Function is true if none of the input conditions is fulfilled |
|  |  |  |  | 0 | 0 | 1 |  |
|  |  |  |  | 0 | 1 | 0 |  |
|  |  |  |  | 1 | 0 | 0 |  |
|  |  |  |  | 1 | 1 | 0 |  |
| XOR |  |  | $B \longrightarrow$ | A | B | Y | Function is true if exactly one of the input conditions is fulfilled |
|  |  |  |  | 0 | 0 | 0 |  |
|  |  |  |  | 0 | 1 | 1 |  |
|  |  |  |  | 1 | 1 | 0 |  |
| XNOR |  |  |  | A | B | Y | Function is true if all of the input conditions are fulfilled or all conditions are not fulfilled |
|  |  |  |  | 0 | 0 | 1 |  |
|  |  |  |  | 0 | 1 | 0 |  |
|  |  |  |  | 1 | 0 | 0 |  |
|  |  |  |  | 1 | 1 | 1 |  |

The logic components of the first level may combine up to three, the components of the second level up to four input conditions. If individual inputs are not used, their state is automatically set to a condition which has no influence on the logic result.

### 3.3.3 Limit values

States of limit values are the most important input quantities of the logic module. Depending on the application, limits either monitor the exceeding of a given value (upper limit) or the fall below a given value (lower limit). Limits are defined by means of two parameters, the limit for the ON and the limit for the OFF state. The hysteresis is the difference between these two values.

Upper limit: The limit for ON state (L.Un) is higher than the limit for the OFF state (L.DFF)


- The state 1 (true) results if the limit for ON state is exceeded. It remains until the value falls below the limit for OFF state again.
- The state 0 (false) results if the limit for ON state is not yet reached or if, following the activation of the limit value, the value falls below the limit for OFF state again.

Lower limit: The limit for ON state (L.Dn) is smaller than the limit for OFF state (L.DFF)


- The state 1 (true) results if the value falls below the limit for ON state. It remains until the value exceeds the limit for OFF state again.
- The state 0 (false) results if the value is higher than the limit for ON state or if, following the activation of the limit value, the value exceeds the limit for OFF state again.

If for a limit value the limit for ON state and the limit for OFF state are configured to the same value, it will be treated as an upper limit value with a hysteresis of $0 \%$.

Limit values may be used to control the running of operating hour counters. As long as the limit values are fulfilled (logical 1) the operating hour counters keep on running. Not only operating times may be measured, but e.g. time under overload condition (additional stress) as well.
3.3.4 Sequence of evaluation


The evaluation of the logic module is performed from top to bottom and from left to right:

1. Y1, Y2, Y3, Y4
2. $\mathrm{Z} 1, \mathrm{Z} 2, \mathrm{Z} 3, \mathrm{Z4}$
3. D1, D2, D3, D4
4. $\mathrm{A} 1, \mathrm{~A} 2, \mathrm{~A} 3, \mathrm{~A} 4$

- The evaluation is performed once each cycle of the power frequency, e.g. every 20 ms at 50 Hz . But the time between two evaluations will never be longer than 25 ms .
- If the logical states Y1...Y4, Z1...Z4, D1...D4 and A1...A4 are used as inputs, their changed states will be included in the evaluation of the next interval
- Exception: In the first evaluation level the state of previous logical functions may be used as input without delay, e.g. the state Y1 for the logical functions with output Y2, Y3 or Y4.


### 3.4 Free Modbus image

Accessing measured data of a Modbus device often needs some special effort, if the interesting measurements are stored in different, non continuous register areas. This way multiple telegrams must be sent to the device to read all data. This needs time and it's very likely, that the measurements don't originate from the same measurement cycle.

A free assembly of the data to read helps a lot. The ApLus supports, along with the still available classical Modbus image with thousands of registers, the facility to assemble two different images, which may be read with one telegram only. These freely assembled images are refreshed after each measurement cycle and therefore always provide the most present values.

## The free float image

Up to 60 instantaneous, mean, unbalance or THD/TDD values may be arranged in any sequence on the register addresses 41840-41958. All of these values are floating point numbers, which allocate 2 registers per value. Meter values are not possible because they have another format.

## The free integer image

Some older control systems are not able to handle float values. To make it possible to work with the data of the device up to 20 16-Bit integer values can be derived from the existing measurement values. These values will then be stored in the free Modbus image (register 41800 up to 41819) as integer values with selectable range of values.

Example: Current transformer 100/5A, measurement current phase 1, over range 20\%

- The reference value is 120A (maximum measurable current)
- The integer value shall be 12 '000 if the measurement is 120 A

After selecting the measured quantity and entering the register value of 12 '000 automatically a scaling factor of 100.0 is calculated. The measurement I1 therefore will be multiplied by 100.0 before it is converted into an integer value and stored in the Modbus image.

Also in the integer image instantaneous, mean, unbalance or THD/TDD values may be arranged.


For devices with Profibus interface the Modbus image is used for the assembly of the cyclical telegram. Via Modbus the same image can be used, but it's not possible to use it independently.

The Modbus communication of the Aplus is described in a separate document. Depending on the communication hardware selected, either the manual for Modbus/RTU or Modbus/TCP protocol should be used. These documents may be found on the software CD or can be downloaded via our homepage http://www.camillebauer.com.

- W157 695: Modbus/RTU interface Aplus (communication interface RS485)
- W162 636: Modbus/TCP interface Aplus (communication interface Ethernet)


## 4. Mechanical mounting

- The standard version of the APLUs is designed for panel mounting as shown below
- The version without display with top-hat rail adapter may be clipped onto a top-hat rail according to EN50022

Please ensure that the operating temperature limits are not exceeded when determining the place of mounting (place of measurement):

$$
-10 \ldots 55^{\circ} \mathrm{C}
$$

### 4.1 Panel cutout



Dimensional drawing Aplus:
See section 10

### 4.2 Mounting of the device

The Aplus is suitable for panel widths up to 10 mm .

a) Slide the device into the cutout from the outside
b) From the side slide in the mounting clamps into the intended openings and pull them back about 2 mm
c) Tighten the fixation screws until the device is tightly fixed with the panel

### 4.3 Demounting of the device

The demounting of the device may be performed only if all connected wires are out of service. Remove all plug-in terminals and all connections of the current and voltage inputs. Pay attention to the fact, that current transformers must be shortened before removing the current connections to the device. Then demount the device in the opposite order of mounting (4.2).

## 5. Electrical connections

## Ensure under all circumstances that the leads are free of potential when connecting them !

### 5.1 General safety notes



Please observe that the data on the type plate must be adhered to !
The national provisions (e.g. in Germany VDE 0100 "Conditions concerning the erection of heavy current facilities with rated voltages below 1000 V") have to be observed in the installation and material selection of electric lines!


Nameplate of a device equipped with RS485 interface and I/O extension 1

| Symbol | Meaning |
| :--- | :--- |
|  | Device may only be disposed of in a professional manner! |
|  | CE conformity mark. The device fulfills the requirements of the applicable EC <br> directives. See declaration of conformity. |
|  | Caution! General hazard point. Read the operating instructions. |
|  | General symbol: Input |
| CAT III | Measurement category CAT III for current / voltage inputs and power sure mark comply with both the Canadian (CSA) and the American (UL) |
| CAT II | Measurement category CAT II for relay outputs |

### 5.2 Electrical connections of the I/Os

| I/O no. | Terminal | No. | APLUS | I/O extension 1 | I/O extension 2 |
| :--- | :--- | :--- | :--- | :--- | :--- |
| 1 | X2 | $1,2,3$ | Relay |  |  |
| 2 | X3 | 1,2 | Digital input |  |  |
| 3 | X3 | 3,4 | Digital output |  | Relay |
| 4 | X5 | $1,2,3$ |  | Relay | Relay |
| 5 | X6 | $1,2,3$ |  | Relay | Digital I/O |
| 6 | X7 | 1,2 |  | Digital I/O | Digital I/O |
| 7 | X7 | 3,4 |  | Digital I/O | Digital I/O |
| 8 | X7 | 5,6 |  | Analog output $\pm 20 \mathrm{~mA}$ | Digital I/O |
| 9 | X7 | 7,8 |  | Analog output $\pm 20 \mathrm{~mA}$ | output $\pm 20 \mathrm{~mA}$ |
| 10 | X7 | 9,10 |  | Digital I/O |  |
| 11 | X7 | 11,12 |  | Digital I/O |  |

I/O no. - as used in the CB-Manager software

### 5.3 Possible cross sections and tightening torques



Inputs L1, L2, L3, N, I1 k-I, I2 k-I, I3 k-I
Single wire
$1 \times 0,5 \ldots 4,0 \mathrm{~mm}^{2}$ or $2 \times 0,5 \ldots 2,5 \mathrm{~mm}^{2}$
Multiwire with end splices
$1 \times 0,5 \ldots 2,5 \mathrm{~mm}^{2}$ or $2 \times 0,5 \ldots 1,5 \mathrm{~mm}^{2}$
Tightening torque
$0,5 \ldots 0,6 \mathrm{Nm}$ resp. 4,42...5,31 lbf in
Power supply X1, Relays X2, X5, X6
Single wire
$1 \times 0,5 \ldots 2,5 \mathrm{~mm}^{2}$ or $2 \times 0,5 \ldots 1,0 \mathrm{~mm}^{2}$
Multiwire with end splices
$1 \times 0,5 \ldots 2,5 \mathrm{~mm}^{2}$ or $2 \times 0,5 \ldots 1,5 \mathrm{~mm}^{2}$
Tightening torque
$0,5 \ldots 0,6 \mathrm{Nm}$ resp. 4,42...5,31 lbf in
I/O's X3, X7 and RS485 connector X4
Single wire
$1 \times 0,5 \ldots 1,5 \mathrm{~mm}^{2}$ or $2 \times 0,25 \ldots 0,75 \mathrm{~mm}^{2}$
Multiwire with end splices
$1 \times 0,5 \ldots 1,0 \mathrm{~mm}^{2}$ or $2 \times 0,25 \ldots 0,5 \mathrm{~mm}^{2}$
Tightening torque
$0,2 \ldots 0,25 \mathrm{Nm}$ resp. 1,77...2,21 lbf in

### 5.4 Inputs

All voltage measurement inputs must originate at circuit breakers or fuses rated 10 Amps or
less. This does not apply to the neutral connector. You have to provide a method for
manually removing power from the device, such as a clearly labeled circuit breaker or a
fused disconnect switch.
When using voltage transformers you have to ensure that their secondary connections
never will be short-circuited.

## No fuse may be connected upstream of the current measurement inputs !

When using current transformers their secondary connectors must be short-circuited during installation and before removing the device. Never open the secondary circuit under load.

The connection of the inputs depends on the configured system (connection type). The required device external fusing of the voltage inputs is not shown in the following connection diagrams.


Direct connection


With current and voltage transformer


With current transformer


In case of current measurement via L2 or L3 connect voltages according to the following table:

| Current | Terminals |  | L1 | L2 | L3 |
| :---: | :---: | :---: | :---: | :---: | :---: |
| L2 | $11-k$ | $11-I$ | L2 | L3 | L1 |
| $L 3$ | $11-k$ | $11-I$ | $L 3$ | L1 | L2 |



By rotating the voltage connections the measurements U12, U23 and U31 will be assigned interchanged!

Four wire system, balanced load, current measurement via L1

Direct connection


With current and voltage transformer


With current transformer


In case of current measurement via L2 or L3 connect voltages according to the following table:

| Current | Terminals |  | L1 | $\boldsymbol{N}$ |
| :---: | :---: | :---: | :---: | :---: |
| L2 | I1-k | I1-I | L2 | $N$ |
| L3 | I1-k | I1-I | L3 | $N$ |

## Direct connection



With current transformers


With current and 3 single-pole isolated voltage transformers


Three wire system, unbalanced load, Aron connection

Direct connection


With current and 3 single-pole isolated voltage transformers


Direct connection


With current transformers


With current and 3 single-pole isolated voltage transformers


## Four wire system, unbalanced load, Open-Y

Direct connection


With current transformers


With current and 2 single-pole isolated voltage transformers


Direct connection


With current transformers


### 5.5 Rogowski current inputs

The connection of the Rogowski coils is performed depending on the selected system type, as shown in chapter 5.4 above. However, instead of current transformers a Rogowski coils is placed around each current-carrying conductor. This is subsequently shown for the measurement in a 4-wire low-voltage system.


In order to suppress injected interferences the shielding (green) is connected always to the I terminal of the current inputs (I1-I, I2-I resp. I3-I).

### 5.6 Power supply



A marked and easily accessible current limiting switch has to be arranged in the vicinity of the device for turning off the power supply. Fusing should be 10 Amps or less and must be rated for the available voltage and fault current.

### 5.7 Relays



When the device is switched off the relay contacts are de-energized, but dangerous voltages may be present.


The relay X 2 is part of the basic unit and therefore always available. The relays X 5 and X 6 are provided for device versions with I/O extension PCB only.
The plug-in terminals have different colours to prevent mixing up the connections. The pin assignment is the same for all relays:


### 5.8 Digital inputs and outputs

For the digital inputs / outputs an external power supply of 12 / 24 V DC is required.


The power supply shall not exceed 30 V DC !


The plug-in terminal X 7 is available for device versions with I/O extension PCB only.
The number of digital inputs / outputs varies depending on the optional built-in PCB, see nameplate. The operating direction of the digital I/Os on X7 may be individually selected by means of the PC software.

$\square$
The assignment of the connections depends on whether an I/O is configured to be a digital input or a digital output.

## Example

Device with I/O extension 2 (2 relays +6 digital I/Os)
The digital I/Os on plug-in terminal X7 are individually programmable as input - or output $\Theta$

On plug-in terminal X3 a digital input and a digital output are provided statically. Their operating direction may not be modified.

## Usage as digital input

- Meter tariff switching
- Operating feedback of loads for operating time counters
- Trigger and release signal for logic module
- Pulse input for meters of any kind of energy
- Clock synchronization
- Synchronization of billing intervals in accordance with energy provider

12 / 24V DC


| Technical data | $<7,0 \mathrm{~mA}$ |
| :--- | :--- |
| Input current | $\leq 16 \mathrm{~Hz}$ |
| Counting frequency (SO) | -3 up to +5 V |
| Logical ZERO | 8 up to 30 V |
| Logical ONE |  |

## Usage as digital output

- Alarm output for logic module
- State reporting
- Pulse output to an external counter (acc. EN62053-31)
- Remote controllable state output via bus interface


## Driving a relay



Driving a counter mechanism


1) Recommended if input impedance
of counter > $100 \mathrm{k} \Omega$

| Technical data |  |
| :---: | :---: |
| Rated current | 50 mA ( 60 mA max.) |
| Switching frequency (SO) | $\leq 20 \mathrm{~Hz}$ |
| Leakage current | 0,01 mA |
| Voltage drop | $<3 \mathrm{~V}$ |
| Load capacity | $400 \Omega \ldots 1 \mathrm{M} \Omega$ |



The width of the energy pulses can be selected by means of the PC software but have to be adapted to the counter mechanism. Once a second there is a decision how many pulses have to be output. Therefore the delay between two pulses may not be used to determine the present power demand.
Electro mechanical meters typically need a pulse width of $50 . . .100 \mathrm{~ms}$.

Electronic meters are partly capable to detect pulses in the kHz range. There are the types NPN (active negative edge) and PNP (active positive edge). For the APLUS a PNP type is required. The pulse width has to be at least 30 ms (acc. EN62053-31). The delay between to pulses corresponds at least to the pulse width. The smaller the pulse width, the higher the sensitivity to disturbances.

### 5.9 Analog outputs

Analog outputs are available for devices with I/O extension 1 only. See nameplate.


## Connection to an analog input card of a PLC or a control system

The Aplus is an isolated measurement device. In addition the particular outputs are galvanically isolated. To reduce the influence of disturbances shielded a twisted-pair cables should be used. The shield should be connected to earth on both opposite ends. If there a potential differences between the ends of the cable the shield should be earthed on one side only to prevent from equalizing currents.
Under all circumstances consider as well appropriate remarks in the instruction manual of the system to connect.

### 5.10 Modbus interface RS485 X4 and / or X8

Depending on the device version up to two Modbus interfaces are available on the plug-in positions X4 and / or X8. These are galvanically isolated. The connection terminals are distinguished by color: X4 (gray), X8 (black).


The signal wires (X4-1, X4-2 resp. X8-1, X8-2) have to be twisted. GND (X4-3 resp. X8-3) can be connected via a wire or via the cable screen. In disturbed environments shielded cables must be used. Supply resistors (Rs) have to be present in bus master (PC) interface. Stubs should be avoided when connecting the devices. A pure daisy chain network is ideal.

You may connect up to 32 Modbus devices to each bus. A proper operation requires that all devices connected to the respective bus have equal communication settings (baud rate, transmission format) and unique Modbus addresses. If there are two Modbus interfaces, their settings may be different.

The bus system is operated half duplex and may be extended to a maximum length of 1200 m without repeater.

### 5.11 Profibus DP interface



The 9-pin DSUB socket serves the connection of a standard Profibus plug. In a bus terminal device, the bus line must be terminated with resistors in the bus plug. Then standard pin assignment is as follows:

| Pin | Name | Description |
| :---: | :---: | :--- |
| 3 | B | RxD/TxD-P |
| 4 | RTS | Request to send: CNTR-P (TTL) |
| 5 | GND | Data ground |
| 6 | $+5 V$ | VP |
| 8 | A | RxD/TxD-N |

## LED BF (Bus failure, yellow)

| Status | Description |
| :--- | :--- |
| ON | Startup state or internal communication error |
| Flashing $(2 \mathrm{~Hz})$ | Parameterization check failed |
| OFF | Cyclical operation; no error |

LED BA (Bus alive, green)

| Status | Description |
| :--- | :--- |
| OFF | Startup state; no Profibus communication |
| Flashing $(2 \mathrm{~Hz})$ | Profibus detected; waiting for parameterization from master |
| ON | Parameterization ok; Profibus communication active |

## 6. Commissioning

Before commissioning you have to check if the connection data of the transducer match the

0data of the plant (see nameplate).

If so, you can start to put the device into operation by switching on the power supply and the measurement inputs

$\bullet$ Measurement input
Input voltage
Input current
System frequency
1 Works no.
2 Test and conformity marks
3 Assignment voltage inputs
4 Assignment current inputs
5 Assignment power supply
6 Load capacity relay outputs

### 6.1 Software installation CB-Manager

A complete parametrization of the device is possible via configuration interface only, using the supplied PC software CB-Manager. The software may also be downloaded free of charge from our homepage http://www.camillebauer.com .

The file "Read-me-first" on the Doku-CD provides all necessary information for the installation of the CB-Manager software and assistance for possible problems.

## Functionality of the CB-Manager software

The software is primary a tool for the configuration of different devices (ApLus, CAM, VR660, A200R, V604s) and supports the user during commissioning and service. It allows as well the reading and visualization of measured data

- Acquisition and modification of all device features
- Setting of real-time clock and time zone, selection of time synchronization method
- Archiving of configuration and measurement files
- Visualization of present measurements
- Reading, setting and resetting of meters
-Reading and resetting of minimum/maximum values
- Starting, stopping and resetting of the optional data logger
- Recording of measurement progressions during commissioning
- Check for correct device connection
- Simulation of states or outputs to test subsequent circuits
- Adjust the security system as protection against unauthorized access or manipulations

The CB-Manager software provides a comprehensive help facility, which describes in detail the operation of the software as well as all possible parameter settings.

### 6.2 Parametrization of the device functionality

## Operating the software

The device configuration is divided into registers, which contain thematically the different function blocks of the device, e.g. "input", "limit values", "display". Thereby of course there are interdependencies, which have to be considered. If e.g. a current limit value is defined and subsequently the ratio of the current transformer is changed, there is a high probability that the limit value is changed as well. Therefore a meaningful sequence must be kept during setting the parameters. The easiest way is to handle register by register and line by line:

- Device (set the device version, if not read directly from the device)

If an I/O extension unit is used: Fix the data direction of the digital I/O's. Do to so just click on the appropriate entry and change the data direction in the I/O register. So it's assured that these I/O's can be used in the intended way. If e.g. you miss to change de basic setting "digital input" the appropriate channel can't be used as output in the logic module.

- Input, especially system and transformer ratios
- Mean values >> Limit values >> Logic module >> I/O 1-3
$\rightarrow$ if present: I/O 4,5 >> I/O 6,7 >> I/O 8,9 >> I/O 10,11


## - Operating hours

- if present: Logger >> Interface (Ethernet, Profibus DP) >> Display
- Modbus-Image (if you want to define your own Modbus image)
- Time zone (for automatical handling of daylight saving time)



## ONLINE / OFFLINE

The parametrization may be performed ONLINE (with existing connection to the device) or OFFLINE (without connection to the device). To perform an ONLINE configuration first the configuration of the connected device, and therewith its hardware version, is read. A modified configuration can then be downloaded to the device and stored on the hard disk of the computer for archiving.

An OFFLINE parametrization can be used to prepare device configurations, to store them on disk and to download it to the devices, once you are in the field where the devices are installed. To make this work, the device versions selected during parametrization must agree with the versions on site.

### 6.3 Installation check

## Check if inputs are connected correctly

- Voltage (at least $20 \% U_{\text {rated }}$ ) and current (at least $2 \% I_{\text {rated }}$ ) must be present

Using the connection check, which is integrated in the visualization of the instantaneous values, the correct connection of the current and voltage inputs may be checked. The phase sequence will be checked, as well as if there are open connections or reversed current connections (which change the direction of the current).

The image below shows open current connections (red description I1, I2, I3). This arises because the individual currents are below $2 \%$ of the rated value.


## Simulation of I/O's

To check if subsequent circuits will work properly with the measurement data provided by the APLUS all analog, digital and relay outputs may be simulated, by predefining any output value resp. discrete state by means of the CB-Manager software.

Also all functions of the logic module, which allows performing any combination of logical states, may be predefined. This way e.g. an alarming due to a violation of a limit value can be simulated.

### 6.4 Installation of Ethernet devices

### 6.4.1 Connection

Before devices can be connected to an existing Ethernet network, you have to ensure that they will not disturb the normal network service. The rule is:

## None of the devices to connect is allowed to have the same IP address than another device already installed

The factory setting of the IP address of ApLUS is: 192.168.1.101

The standard RJ45 connector serves for direct connecting an Ethernet cable. If the PC is directly connected to the device a cross-wired cable must be used.

The network installation of the devices is done by means of the CB-Manager software (see 6.4.2) or directly via the local programming on the display. As soon as all devices have a unique network address they may be accessed by means of a suitable Modbus master client.

- Interface: RJ45 connector, Ethernet 100BaseTX
- Mode: $\quad 10 / 100 \mathrm{MBit} / \mathrm{s}$, full / half duplex, Auto-negotiation
- Protocols: Modbus/TCP, NTP


Function of the LED's

| LED 1 (Green) | - ON as soon as a network connection exists <br> - Flashing when data is transmitted via Ethernet connection |
| :--- | :--- |
| LED 2 (Orange) | - Flashing with 4 Hz during start-up <br> - ON during Modbus/TCP communication with the device |



To have a unique identification of Ethernet devices in a network, to each connection a unique MAC address is assigned. This address is given on the nameplate, in the example 00-12-34-AE-00-01.

Compared to the IP address, which may be modified by the user any time, the MAC address is statically.

### 6.4.2 Network installation using the CB-Manager software

For the subsequent Modbus/TCP communication a unique network address must be assigned to each of the devices. This can be done very easily, using the CB-Manager software to search for devices which have a MAC address 00-12-34-AE-xx-xx, which identifies the device as APLUs of Camille Bauer. Because this is performed by means of a UDP broadcast telegram, the devices are allowed to have the same network address at the beginning, e.g. "192.168.1.101" as factory default.

As soon as to all the devices network settings with unique IP address have been assigned, they may be accessed and read using the Modbus/TCP protocol


Select "settings" under options | interface. The interface type has to be set to "TCP-IP".

## Devices in the local network



Set settings to "CAM, APLUS". Along with all Aplus also SINEAX CAM devices installed in the same network will be shown. The identification of the devices is possible by means of their MAC address, which is given on the nameplate (see chapter 6.4.1).

To assign a unique network address to a device, select it in the list and the click on "change".

## Configure:

$\Gamma$ Gateway


The following settings have to be arranged with the network administrator:

- IP address: This one must be unique, i.e. may be assigned in the network only once.
- Subnet mask: Defines how many devices are directly addressable in the network. This setting is equal for all the devices.
- Default gateway: Is used to resolve addresses during communication between different networks. Should contain a valid address within the own network.
- Hostname: Individual designation for each device. Helps to identify the device in the device list.


## Example



## Devices outside the local network



Devices which are not in the same network as the PC (e.g. in the Internet) can not be found and have to be added manually to the device list by means of $\square+$. The type of the device must be selected previously. To each entry you have to assign a unique IP and MAC address, which are different from the initial value. Otherwise it's not possible to add further entries.
The setting of the network parameters must be performed before mounting the device. As an alternative this may be done in the destination network via Ethernet interface.

### 6.4.3 Network installation by means of local programming

The network settings IP address, subnet mask and gateway can also be configured directly via the local programming of the APLUS on site.

This facility is shown in chapter $\underline{7.8}$

### 6.4.4 Time synchronization via NTP-protocol

For the time synchronization via Ethernet NTP (Network Time Protocol) is the standard. Corresponding time servers are used in computer networks, but are also available for free via Internet. Using NTP it's possible to hold all devices on a common time base.

Two different NTP servers may be defined. If the first server is not available the second server is used for trying to synchronize the time. Adjusting of the clock is performed in the interval selected ( 15 min . up to 24h). If no time synchronization is desired, to both NTP servers the address 0.0.0.0 have to be assigned.
The setting of the addresses is done by means of the CB-Manager software. The NTP data is arranged in the register "Ethernet" of the device configuration.

## Activation

To activate the time synchronization via NTP, the "Synchronisation RTC" must be checked by means of the checkbox.

| Gerät | Eingang | Mittelwerte | Grenzwert | Logikmodul | $1 / 0$ | . |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1/0 8,9 | I/O 10.11 | Betriebsstunden | Logger | Störschreiber | Ethernet | Anzeig |

Einstellungen

| IP Adresse | 192.168.57.251 |
| :---: | :---: |
| Subnet-Maske | 255.255 .252 .0 |
| Gateway | 192.168.56.4 |
| NTP Server 1 | 192.168.56.56 |
| NTP Server 2 | 0.0.0.0 |
| Synchronisation RTC | $\sqrt{\square}$ NTP Server |
| MODBUS TCP Port | - 502 |
| MAC Adresse | 001234 AE 0007 |

### 6.4.5 TCP ports for data transmission

## TCP ports

The TCP communication is done via so-called ports. The number of the used port allows determining the type of communication. As a standard Modbus/TCP communication is performed via TCP port 502, NTP uses port 123. However, the port for the Modbus/TCP telegrams may be modified. You may provide a unique port to each of the devices, e.g. 503, 504, 505 etc., for an easier analysis of the telegram traffic. The setting of the Modbus TCP port is done as shown above. Independent of these setting a communication via port 502 is always supported. The device allows at least 5 connections to different clients at the same time.

## Firewall

Due to security reasons nowadays each network is protected by means of a firewall. When configuring the firewall you have to decide which communication is desired and which have to be blocked. The TCP port 502 for the Modbus/TCP communication normally is considered to be unsafe and is very often disabled. This may lead to a situation where no communication between networks (e.g. via Internet) is possible.

### 6.5 Installation of Profibus DP devices

The Profibus DP interface allows data exchange with a control system via Profibus-DP V0. The modular device model provides maximum protocol efficiency.

Required measured variables are determined during engineering and arranged as a fixed process image. The control system does not require any intelligence for the evaluation of the data (no tunneling protocol).

Bus parameterising facilitates simple and fast commissioning. On-site the parameters in accordance with the configuration menu can be set, especially:

- Device address
- Accepting master parameterization (Check_User_Prm)
- Establishing communication to the master (Go_Online)
- Setting device address via master (Set_Slave_Addr_Supp)

For the assembly of the cyclical Profibus telegram the Modbus image is used. Via Modbus the same image can be used, but it's no longer possible to use it independently.

## GSD parameterization

Typically the parameterization of the Profibus slave is done on the control system. During startup the Aplus adopts these settings. Doing so the parameterization of the input parameters (input system, transformer ratios etc.) as well as the assembly of the Modbus image will be overwritten. Other parts of the configuration, such as parameterization of I/O's or settings of limit values, remain unchanged.
All necessary informations for the parameterization are part of the DMF file. This one can be loaded from the Doku-CD supplied with the Aplus.
The assumption of the engineered parameters can be prevented by deactivating the Check_User_Prm flag. The parameterization locally set will not be changed this way.

## Cyclical data exchange

The user can compose its own „station" with all required quantities. Up to 60 measured quantities can be modularly concatenated. You may choose from instantaneous values of the system and imbalance analysis, mean-values of power quantities and freely selectable quantities as well as meter values.

Subsequent to the adoption of the parameterization, the APLUS is ready for the cyclical data exchange with the control system.

### 6.6 Protection against device data changing

Data stored in the device may be modified or reset via communication interface or via the keys on the device itself. To restrict these possibilities on-site, via CB-Manager the security system in the device can be activated (factory default: not activated). For the definition of these user rights in the software the input of an administrator login is required. The factory default is:

| user:  <br> password: admin | The administrator password may be modified, but a <br> reset can be performed in our factory only! |
| :--- | :--- | :--- |

For one user via device and one user via interface (special login) the access to the following functions can individually be granted: Configuration of the device, modification of RTC parameters, modification of limit values, reset of min/max or meter values, alarm acknowledgment, display mode changing.

## 7. Operating the device

### 7.1 Display and operating elements



| (1) | $\begin{aligned} & 12 \underline{Z} \\ & 3 N \end{aligned}$ | Phase reference of measurement, sign of measurement, minimum or maximum value, e.g. $\mathrm{U}_{1 \mathrm{~N}}$ (maximum value) |
| :---: | :---: | :---: |
| (2) | $\begin{aligned} & \text { ㄷ․ } 4.4 \\ & \text { ロ } \end{aligned}$ | 4-digit display of measurements. On each change of the measurement display the short form of the quantities to display is shown first. <br> If a measurement is out of the measurable range the string "oL" is shown instead of a measured value. |
| (3) | kMGE\%ø WArHzk上 | Unit, measuring procedure, measurement type e.g. kVAr (reactive power) |
| (4) | PCIWER FRR | 8 -digit meter display, 4-digit measurement display (P,Q,S,U,I) or 20-digit Alarm text display (e.g. "POWER FAILURE L1") |
| (5) | $\begin{aligned} & \text { KMG } \begin{array}{l} \text { K } \\ \text { WArh } \end{array} \text {. } \end{aligned}$ | Unit for meter quantities, high or low tariff, e.g. MWh high tariff Unit for the quantities $\mathrm{Px}, \mathrm{Qx}, \mathrm{Sx}, \mathrm{Ux}, \mathrm{Ix}$ |
| (6) | -000 | State display of alarms, e.g. Alarm 1 active |
| (7) | i short <br> i $>2 \mathrm{~s}$ | Display of alarm state texts <br> Reading of meter contents |
| (8) | - 4 ¢ | Functionality depends on operating time, either 'short' or $>2 \mathrm{~s}$. To be used for measurement selection, brightness adjustment, navigation in menus, reset operations. |

### 7.2 Operating modes

The device supports, along with the configuration mode, three different operating modes. Normally the device is in the measurement display mode, but may be temporarily switched for the reading of the meters or for the display of alarm texts.



Measurement display: Is the normal operating mode of the device. By means of the navigation keys different measurement display can be selected. Depending on the selected display mode and the system monitored different measurement displays are available.

Available display modes

Meter reading: By pressing the key $\mathbf{i}$ for a longer time an operating mode is started, which allows to read all the meter contents via line 4. This mode is automatically stopped after 30s without any key pressed or via the key $\stackrel{\text { ESC }}{\longleftrightarrow}$. If this mode is active no measurement info is displayed on line 1 to 3.
Meter reading

Alarm display: By shortly pressing the key $\mathbf{i}$ an operating mode is started, which allows to display alarm state texts and to acknowledge alarms via line 4. If there are no configured alarms the message "No LED used" is displayed and then the mode is stopped. Otherwise the mode is automatically stopped after 30s without any key pressed or via the key ${ }^{\text {ESC }}$ no measurement info is displayed on line 1 to.

- Monitoring and alarming
- Alarm handling


### 7.3 Setting the display brightness

The brightness of the display can be set to one of thirteen levels.


Brighter: Press key $\uparrow$ longer than 2 s ; brightness will increase in steps
Darker: Press key longer than 2s; brightness will decrease in steps

### 7.4 Display modes

The device supports four different display modes. They differ in the way measurement data is presented and which measurement data is provided.

- The selection of the display mode is described under Configuration


## FULL mode

The measurement images of all displayable data are arranged in a matrix form. The selection is performed by means of the arrow keys:

| $\boldsymbol{4}$ | One image to the left. If first: most right image is displayed |
| :---: | :--- |
| $\boldsymbol{4}$ | Most left image of the next line is displayed. If last: First line. |
| $\boldsymbol{A}$ | Most left image of the previous line is displayed. If first: Last line. |
| $\boldsymbol{\rightarrow}$ | One image to the right. If last: most left image is displayed |

The fourth line of each image is allocated to a programmable meter value (METER), which does not change even if another measurement image is selected.

The complete display matrices are shown in Annex B

| $\begin{aligned} & \hline \text { U12 } \\ & \text { U23 } \\ & \text { U31 } \\ & \text { METER } \end{aligned}$ | U12_MAX <br> U23_MAX <br> U31_MAX <br> METER | U12_MIN <br> U23_MIUN <br> U31_MIN <br> METER | $\begin{aligned} & \text { DEV_UMAX } \\ & \text { DEV_UMAX_MAX } \end{aligned}$ <br> METER |  |
| :---: | :---: | :---: | :---: | :---: |
| UR1 <br> UR2 <br> U0 <br> METER | $\begin{aligned} & \text { UNB_UR2_UR1 } \\ & \text { UNB_UR2_UR1_MAX } \end{aligned}$ <br> METER |  |  |  |
| $\begin{aligned} & \hline 11 \\ & 12 \\ & 13 \\ & \text { METER } \end{aligned}$ | I1_MAX <br> 12_MAX <br> I3_MAX <br> METER | $\begin{array}{\|l\|} \hline \text { IB1 } \\ \text { IB2 } \\ \text { IB3 } \\ \text { METER } \end{array}$ | IB1_MAX <br> IB2_MAX <br> IB3_MAX <br> METER | $\begin{aligned} & \hline \text { DEV_IMAX } \\ & \text { DEV_IMAX_MAX } \end{aligned}$ <br> METER |
| $\begin{aligned} & \hline \text { IR1 } \\ & \text { IR2 } \\ & \text { IO } \\ & \text { METER } \end{aligned}$ | $\begin{aligned} & \hline \text { UNB_IR2_IR1 } \\ & \text { UNB_IR2_IR1_MAX } \end{aligned}$ <br> METER |  |  |  |
| P P_MAX <br> METER |  |  |  |  |
| $\begin{aligned} & \hline \text { Q } \\ & \text { Q_MAX } \end{aligned}$ <br> METER |  |  |  |  |
| S <br> S_MAX <br> METER |  |  |  |  |
| PF <br> PF_MIN_IN_L <br> PF_MIN_IN_C <br> METER | PF <br> PF_MIN_OUT_L <br> PF_MIN_OUT_C <br> METER | PFG <br> PFG_MIN_IN_L <br> PFG_MIN_IN_C <br> METER | PFG <br> PFG_MIN_OUT_L <br> PFG_MIN_OUT_C <br> METER |  |
| $\begin{aligned} & \hline \text { F_MAX } \\ & \text { F } \\ & \text { F_MIN } \\ & \text { METER } \end{aligned}$ |  |  |  |  |
| $\begin{aligned} & \hline \mathrm{P} \\ & \mathrm{Q} \\ & \mathrm{~S} \\ & \text { METER } \end{aligned}$ | U_MEAN <br> I_MEAN <br> P <br> METER | $\begin{array}{\|l\|} \hline \text { PF } \\ P \\ Q \\ \text { METER } \end{array}$ | $\begin{array}{\|l\|} \hline P \\ S \\ F \\ \text { METER } \end{array}$ |  |
| D <br> D_MAX <br> METER | QG QG_MAX <br> METER |  |  |  |
| dd.mm <br> hh.mm <br> ss <br> METER | OPR_CNTR1 OPR_CNTR2 OPR_CNTR3 METER | OPR_CNTR <br> METER |  |  |
| $\begin{aligned} & \hline \text { THD_U12 } \\ & \text { THD_U12_MAX } \end{aligned}$ <br> METER | $\begin{aligned} & \hline \text { THD_U23 } \\ & \text { THD_U23_MAX } \end{aligned}$ <br> METER | $\begin{array}{\|l\|} \hline \text { THD_U31 } \\ \text { THD_U31_MAX } \end{array}$ <br> METER |  |  |
| $\begin{aligned} & \hline \text { TDD_I1 } \\ & \text { TDD_11_MAX } \end{aligned}$ <br> METER | $\begin{aligned} & \hline \text { TDD_I2 } \\ & \text { TDD_I2_MAX } \end{aligned}$ <br> METER | $\begin{aligned} & \hline \text { TDD_I3 } \\ & \text { TDD_I3_MAX } \end{aligned}$ <br> METER |  |  |

Example for 3-wire system, unbalanced load (harmonics and power mean-values not shown)

## REDUCED mode

This display mode is a reduced version of the FULL mode. Some of the images or complete lines, e.g. the grayed data in the below example, can be hidden. So the display may be adapted easily to the information requirements on-site.

The selection of the measurement images is done via the arrow keys:

- One image to the left. If first: most right image is displayed
- Most left image of the next line is displayed. If last: First line.

1 Most left image of the previous line is displayed. If first: Last line.
$\Rightarrow$ One image to the right. If last: most left image is displayed
The fourth line of each image is allocated to a programmable meter value (METER), which does not change even if another measurement image is selected.

| U12 U23 U31 METER | U12_MAX <br> U23_MAX <br> U31_MAX <br> METER | U12_MIN U23_MIUN U31_MIN METER | $\begin{aligned} & \hline \text { DEV_UMAX } \\ & \text { DEV_UMAX_MAX } \end{aligned}$ <br> METER |  |
| :---: | :---: | :---: | :---: | :---: |
| UR1 <br> UR2 <br> U0 <br> METER | $\begin{aligned} & \text { UNB_UR2_UR1 } \\ & \text { UNB_UR2_UR1_MAX } \end{aligned}$ <br> METER |  |  |  |
| $\begin{aligned} & \hline 11 \\ & 12 \\ & 13 \\ & \text { METER } \end{aligned}$ | I1_MAX <br> I2_MAX <br> 13_MAX <br> METER | $\begin{aligned} & \hline \text { IB1 } \\ & \text { IB2 } \\ & \text { IB3 } \\ & \text { METER } \end{aligned}$ | IB1_MAX IB2_MAX IB3_MAX METER | $\begin{aligned} & \hline \text { DEV_IMAX } \\ & \text { DEV_IMAX_MAX } \end{aligned}$ <br> METER |
| IR1 <br> IR2 <br> 10 <br> METER | $\begin{aligned} & \hline \text { UNB_IR2_IR1 } \\ & \text { UNB_IR2_IR1_MAX } \end{aligned}$ <br> METER |  |  |  |
| P_MAX <br> METER |  |  |  |  |
| Q_MAX |  | $\Omega$ |  |  |
| S <br> S_MAX <br> METER |  |  |  |  |
| PF <br> PF_MIN_IN_L <br> PF_MIN_IN_C <br> METER | PF <br> PF_MIN_OUT_L <br> PF_MIN_OUT_C <br> METER | PFG <br> PFG_MIN_IN_L <br> PFG_MIN_IN_C <br> METER | PFG <br> PFG_MIN_OUT_L <br> PFG_MIN_OUT_C <br> METER |  |
| $\begin{aligned} & \text { F_MAX } \\ & \text { F } \\ & \text { F_MIN } \\ & \text { METER } \end{aligned}$ |  |  |  |  |
| $\begin{aligned} & \hline \mathrm{P} \\ & \mathrm{Q} \\ & \mathrm{~S} \\ & \text { METER } \end{aligned}$ | U_MEAN <br> I_MEAN <br> P <br> METER | PF <br> P <br> Q <br> METER | $\begin{aligned} & \hline P \\ & S \\ & F \\ & \text { METER } \end{aligned}$ |  |
| D <br> D_MAX <br> METER | QG QG_MAX <br> METER |  |  |  |
| dd.mm hh.mm ss METER | OPR_CNTR1 OPR_CNTR2 OPR_CNTR3 METER | OPR_CNTR <br> METER |  |  |
| $\begin{aligned} & \text { THD_U12 } \\ & \text { THD_U12_MAX } \end{aligned}$ <br> METER | $\begin{aligned} & \hline \text { THD_U23 } \\ & \text { THD_U23_MAX } \end{aligned}$ <br> METER | $\begin{aligned} & \hline \text { THD_U31 } \\ & \text { THD_U31_MAX } \end{aligned}$ <br> METER |  |  |
| $\begin{aligned} & \hline \text { TDD_I1 } \\ & \text { TDD_I1_MAX } \end{aligned}$ <br> METER | $\begin{aligned} & \hline \text { TDD_I2 } \\ & \text { TDD_12_MAX } \end{aligned}$ <br> METER | $\begin{aligned} & \hline \text { TDD_I3 } \\ & \text { TDD_13_MAX } \end{aligned}$ <br> METER |  |  |

Example for 3-wire system, unbalanced load (harmonics and power mean-values not shown)

## USER mode

This display mode allows a free assembly of up to 20 measurement images. Also the fourth line may be different for each image. Any meter value or another quantity ( $\mathrm{Ux}, \mathrm{Ix}, \mathrm{Px}, \mathrm{Qx}, \mathrm{Sx}$ ) may be assigned. The images are arranged among each other and selectable via the keys 1 and :
t Image of the next line is displayed. If last: First line.
个 Image of the previous line is displayed. If first: Last line.
The USER mode also allows defining one of the 20 measurement images to be a predefined image, which is displayed always after a programmable time without user action. This switch back is performed even if in the meantime a change to the FULL or REDUCED mode was performed. This way an always equal appearance of the device can be defined in advance.


## Example with 8 free assembled measurement images

## LOOP mode

In the LOOP mode all of the measurement images of the USER mode are displayed one after the other with a programmable time delay. When a change to the LOOP mode is performed a possibly active preference display (USER mode) is deactivated. When leaving the LOOP mode the preference display is activated again.

USER and LOOP mode can be activated only, if at least one free measurement image has been defined !

### 7.5 Meter reading

A reading of the meter contents may be performed at any time, independent of the present selected display mode. When a meter content is displayed it may be reset to zero if the necessary rights have been granted during the configuration of the device.

Start reading: Press key $\mathbf{i}$ longer than 2 s ;
Stop reading: Press key $\stackrel{\text { EsC }}{4}$;


The first displayed meter is always active energy incoming, high tariff
Using the keys $\uparrow$ and other values from the list of meters may be read as well


After a time of 30 s with no key pressed the meter reading is automatically stopped!

### 7.6 Alarm handling

How alarms are handled is fixed during the configuration of the device. A detailed description about the alarming concept is here:

- Monitoring und alarming


### 7.6.1 Alarm state display on the device

The yellow state LED's are intended for alarming and alarm state display on-site. The displayed states are the result of the state information analysis, defined by the user in the logic module. The type of signaling is comparable to the operating philosophy in control rooms.

| LED | Meaning |
| :--- | :--- |
| OFF | Alarm is not active |
| ON | Alarm is active and acknowledged |
| Fast FLASHING $^{\mathbf{1}}$ | Alarm is active but not yet acknowledged |
| Slow FLASHING $^{\mathbf{1})}$ | Alarm was temporarily active and not yet acknowledged |

${ }^{1)}$ If "acknowledgement of alarm LEDs required" in the logic module configuration is not selected flashing is omitted.


The status display of the LED's is performed only, if the associated logic functions have been configured accordingly

### 7.6.2 Display of alarm texts

The displayed alarm texts are the result of the state information analysis, defined by the user in the logic module. The number of entries in the alarm text list depends on how many logic functions are used. If no function is used, when changing to the alarm display mode an appropriate error message is displayed and then the mode is terminated immediately. If logic functions are defined, the alarm list may contain up to four entries.


To each alarm a state text for the active and the inactive state is assigned. The table of the present alarm state texts contains, depending on the present state, either the text for the active or the inactive alarm. These may be retrieved and displayed on line 4. The first displayed alarm text after starting the alarm text display is the one with the highest priority (see flow diagram, next page).


After a time of 30 s with no key pressed the display of alarm texts is automatically stopped!


### 7.6.3 Acknowledgment of alarms via display

i
Acknowledgment is not required if "acknowledgement of alarm LEDs required" in the logic module configuration is not selected.

The acknowledgment of alarms may be performed via the keys on the device. To do so, the alarm to acknowledge must be actually displayed.
ACKNOWLEDGMENT: Press key (longer than 2s);

| LED before acknowledgment | LED after acknowledgment |
| :--- | :--- |
| \#C: Fast FLASHING | \#B: ON |
| \#D: Slow FLASHING | \#A: OFF |

If the display is configured for alarm reset, the acknowledgment also undoes the possible alarm operation (e.g. the switching of a relay).

## 7．7 Resetting of measurements

The Aplus provides minimum and maximum values of different measured quantities as well as energy meters and operating hour counters．All of them may be reset during operation．

## Basic principle

RESET：Press key（longer than 2 s ）while the quantity to reset is displayed

## Example：Reset of $U_{1 N}{ }_{\text {min }}$ and $U_{1} N_{\text {max }}$

| ${ }^{1} \times \beth 4 \square .5 \text { v }$ | ＞＞Absolute maximum value of U1N since last reset |
| :---: | :---: |
| ${ }^{1} \text { н コココ. }{ }_{\text {v }}$ | ＞＞Present value of U1N |
| ${ }^{1} \mathrm{Nz}$ 己 1 （ 5.5 | ＞＞Absolute minimum value of U1N since last reset |
| 59ヱ． $98{ }_{\text {w }}^{\text {k }}$ | ＞＞Displayed meter content |


| 0 ： | Initial | position as shown above |
| :---: | :---: | :---: |
| 1： | $\underset{\text { clear }}{4}$ | 240．5V starts flashing，on line 4 ［LERR？is flashing as well |
| 2a： | OK | Confirm reset of $\mathrm{U} 1 \mathrm{~N}_{\text {max }}$ ，go to 3 |
| 2 b ： | 7 | No reset of $\mathrm{U}^{1} \mathrm{~N}_{\max }$ ，go to 3 |
| 2c： | ESC | Cancel the reset procedure，go to 4 |
| 3： |  | 210.5 V starts flashing，on line 4 ［LERR T is flashing as well |
| 3a： | OK | Confirm reset of $\mathrm{U}^{1} \mathrm{~N}_{\text {min }}$ ，go to 4 |
| 3b： | 7 | Cancel the reset procedure，go to 4 |
| 4： | Resetting done |  |

## Example：Reset of meter content

| 0 ： | Display | the meter to reset，see Meter reading |
| :---: | :---: | :---: |
| 1： | clear | On line 4 ［LERR is displayed flashing |
| 1a： | OK | Confirm meter reset，go to 2 |
| 1b： | ESC | Cancel meter reset，go to 2 |
| 2 ： | Rücksetzen beendet |  |

Resetting of measurements may be protected via the security system implemented in the device．For further information see protection against device data changing．

### 7.8 Configuration

A complete configuration of the APLUs is possible via CB-Manager software only using the configuration interface of the device. On device side only the parameters described below may be modified. To do so, a configuration menu is provided.

Starting the configuration menu: Press $\underset{\text { menu }}{\Rightarrow}$ (longer than 2 s );


Overview of the navigation structure

## Communication interface c.

$\qquad$
The possible settings depend on the device version selected. The following combinations may be available:

| Bus connection | Menu 1 | Menu 2 |
| :---: | :---: | :---: |
| RS-485 (Modbus/RTU protocol) | c. 485 |  |
| Ethernet (Modbus/TCP protocol) | c. $E+h$ |  |
| RS-485 (Modbus/RTU protocol) + Profibus DP | c. 485 | ${ }_{c} P_{r}-\square$ |
| RS-485 (Modbus/RTU protocol) + RS-485 (Modbus/RTU protocol) | c. 485 | 0485 |
| Ethernet (Modbus/TCP protocol) + RS-485 (Modbus/RTU protocol) | $c E+h$ | -485 |

## -RS-485 (Modbus/RTU interface)

A maximum of two RS-485 interfaces (X4 and / or X8) with Modbus/RTU protocol can be implemented in the device. These interfaces are independent. Their settings may be different, because they are not used in the same Modbus network.


| Menu | Range of values | Description |
| :--- | :--- | :--- |
| Rddr | $1 \ldots .247$ | Modbus device address; must be unique within a Modbus network. |
| bRUd | $2400,4800,9600,19.2 \mathrm{k}$, <br> $38.4 \mathrm{k}, 57.6 \mathrm{k}, 115.2 \mathrm{k} \mathrm{Bd}$ | Transmission speed on the Modbus interface. |
| PRrl | NONE, ODD, EVEN | Parity (none, odd, even) |
| 5.blt | 1Sb, 2Sb | Number of stop bits (Sb) per transmitted data byte. |
| R.LI $\overline{1 /}$ | 0.1S, 64P, 32P, 16P, <br> 8P, 4P, 2P, 1P <br> S=seconds <br> P=pause time | Delay time until the device sends an answer to a Modbus request. <br> The time must be selected the way, that the requesting master is still <br> able to understand the answer. <br> Pause time = "Time to transmit 3.5 characters" |

## - Ethernet (Modbus/TCP-interface)



| Menu | Range of values | Description |
| :--- | :--- | :--- |
| I P | z.B. 192.168 .057 .011 | IP address: Must be unique for each device ! |
| SUL.n | z.B. 255.255 .255 .000 | Subnet mask |
| GRLE | z.B. 192.168 .057 .001 | Gateway address |
| Part | $1 . .65535$ | The TCP port for the Modbus/TCP communication, <br> usually this is port 502. |

## -Profibus DP



| Menu | Range of values | Description |
| :---: | :---: | :---: |
| Rddr | 0... 125 | Device address; must be unique within the Profibus network. |
| bRUd | 9.6 kBd ... 12 MBd | Transmission speed on the Profibus interface. The present value set is displayed (auto detection). |
| c.Prı | Dn / DFF | Check_User_Prm: The parameters of the control system will be used (On) or declined (OFF). Default: On. |
| ח | Dn / DFF | Go_Online: Device is able to connect to the control system (On) or is separated from the Profibus system (OFF). Default: On. |
| SEヒ.R | Dn / DFF | Set_Slave_Addr_Supp: Setting of the device address via Profibus master is allowed (On) or disabled (OFF). Default: On. |
| rES.R | Dn / DFF | If On the device address is reset to the factory setting (126). In this case the device is no longer able to communicate with the control system. |

Further menu parameters

| Menu | Range of values | Description |
| :---: | :---: | :---: |
| M品完 <br> MISロLAM MADE | ```FU゙LL, rEdU' USEr, LOOP see Display modes``` | Display mode of the device．USER and LOOP mode can be activated only，if at least one free measurement image has been defined！ |
| rRLE <br> LPDRTE RRTE ！ms | 100．．． 5000 | Refresh rate of the display．This is the time gap between two updates of the display． |
| LOP.L <br> LOCP TIME ！ | 2．．．10s | The time gap between changes of the displayed measurement image，if the LOOP mode is active． |
| $\text { P.dI } 5$ <br> PREF LSER DISPLRM | 1．．． 20 | Number of the preferred image of the USER mode which is automatically displayed after＂P．tiM＂without user action．LOOP mode must be activated． |
| P.t\| יו <br> PREF RETURN T！ | 10．．． 255 | Time without user action until the USER image＂P．dIS＂is automatically displayed in the LOOP mode． |
| $\begin{aligned} & \text { 5Y5t } \\ & \text { InNロT 5ル5TEM } \end{aligned}$ | see Inputs | System connected to the device．A modification may cause that e．g．limit values or outputs will no longer properly，because the associated measured quantities are no longer valid．Possibly also the existing wiring must be changed． |
| Pri <br> bILTRGE PRIMMRM <br> CURRENT PRIMMRTM | $\begin{aligned} & <1000 \mathrm{MV} \\ & <200.0 \mathrm{kA} \end{aligned}$ | Rated primary voltage of the voltage transformer connected upstream．If the measurement is done directly this value must be the same as＂SEC＂． |
| 5E［ <br> b＇DLTREE SEECMAIRTM <br> CURRENT SEECMMHRTM | 50．．．832V $\mathrm{LL} / 28,9 \ldots 480.3_{\mathrm{LN}}$ <br> 1．．．7．5 A | Rated secondary value of the voltage transformer connected upstream． |
| ヒロP <br> UDLTREE MRI：SEC． <br> CURRENT MAP：5EC． | $\begin{aligned} & S E C \leq t O P \leq(\max . U) \text { or } \\ & S E C \leq t O P \leq(\max . I) \end{aligned}$ | Maximum value which should be measurable on the secondary side of the voltage transformer． <br> Maximum values see＂SEC＂． |
| L.5r[ <br> LIMN：SOUNEE． |  | The measured quantity assigned to the limit value．Can not be modified． $\mathrm{XY}=01 . . .16$ ． |
| L． B п <br>  | Depends on quantity | Limit for $O N$ state of limit value $X Y ; X Y=01 \ldots 16$ ． See Limit values． |
| L.DFF <br>  | Depends on quantity | Limit for OFF state of limit value $\mathrm{XY} ; \mathrm{XY}=01 \ldots 16$ ． See Limit values． |
| $1 \cap F D$ <br> DEMILE InHED TENT |  | Here the configured short description text（TAG）of the device is displayed．Can be modified via CB－Manager only． |
| PG！ <br>  | Dn／DFF | Switch on（On）or off（OFF）recording of power mean values logger． |
|  | Dn／DFF | Switch on（On）or off（OFF）recording of mean values logger． |
| $\overline{\mathrm{IRH}}$ <br> EッTREME WRL LOEGER | Dn／DFF | Switch on（On）or off（OFF）recording of extreme values logger． |
| $\begin{aligned} & \text { MELR } \\ & \text { METER LAGLER? } \end{aligned}$ | Dn／DFF | Switch on（On）or off（OFF）recording of meter logger． |
| $\text { dl } 5 t$ <br> DISTLIRTRANEE PEC． | Dn／DFF | Switch on（On）or off（OFF）recording of disturbance logger． |

## Setting time and date

All time information stored in the device is referenced to UTC ${ }^{1)}$ (Universal Time Coordinated). For a better understanding the time/date information displayed on the display can be converted to local time by defining a time zone offset. This offset is added to the internal UTC time before the time information is displayed. Keep in mind that the offset may be variable if daylight saving time is used locally (see below).

Hint: If time is set via CB-Manager software the difference between local time and UTC rather results from the local time settings of the PC than from the time zone offset configured via display. There may be a discrepancy.

| Menu: Ll $\quad$ LE | Range of values | Description |
| :---: | :---: | :---: |
| 2ロחE <br> TIME IGME DFFGET | -840... 840 [min] | Offset of the local time to UTC time ${ }^{1)}$, which is used as the time reference in the device. |
| $\begin{aligned} & \text { LI ME } \\ & \text { TIME } \end{aligned}$ |  | Setting of hours, minutes and seconds of the built-in real-time clock. |
| $d$ RLE <br> IRTE |  | Setting of day, month and year of the built-in real-time clock. |

## ${ }^{1)}$ UTC (Universal Time Coordinated)

Sometimes UTC is called world time as well. The reference corresponds to the Greenwich Mean Time (GMT). The time zones of the world nowadays are all referenced with an offset to UTC. UTC time doesn't use time shifts, which may occur due to a change to daylight saving time.

Example: In Switzerland the CET (Central European Time) is valid, which has an offset of $+1[\mathrm{~h}]$ to UTC. But during half of the year the CEST (Central European Summer Time) is used, which has an offset of $+2[\mathrm{~h}]$ to the UTC time used in the device.

### 7.8.1 Selection of the parameter to edit

To modify a value you have to navigate through the menu tree by means of the arrow keys until the appropriate parameter is displayed. For the parameter selected on line 4 a detailed description is displayed.

If the description text on line 4 is wider than 8 characters it's shown as a ticker.

| TOdE | >> Previous menu. If blank: End of list |
| :---: | :---: |
| : 5 Cof | >> Presently selectable submenu. Choose via $\Rightarrow$ |
| L1-us | >> Next menu. If blank: End of list |
| CDMFIGURATIGN | >> Description of the submenu of line 2 (ticker) |

Depending on the parameter either a discrete value from a list may be selected or the associated numerical value may be modified.

### 7.8.2 Discrete selection

The configuration of parameters, which can accept a limited number of values only, is implemented by means of selecting a value from a list. In the example shown below to modify the display mode normally the discrete values FULL, REDU, USER and LOOP are available.

Example: Change MODE (DISPLAY MODE) from $r E d U$ to $U S E r$

| FULL | >> 1 Previous element. If blank: End of selection |
| :---: | :---: |
| \% redu | >> Present selection. Change via $\rightarrow$ |
| USEr | - Next element. If blank: End of selection list |
| RETULED MITE | >> Description of the selection on line 2 (ticker) |


| OK | $r E d U$ starts flashing |
| :--- | :--- |
| $\Rightarrow$ | $U S E r$ is displayed flashing as present selection |
| OK | $U 5 E r$ adopted as the new display mode, displayed non flashing |
| $\Rightarrow$ |  |

- The modification mode is left automatically after a time of 15 s with no key pressed and the previous displayed menu is shown again!
- The configuration mode is left automatically after a time of 30 s with no key pressed and the measurement display is shown again!


### 7.8.3 Setting value

For quantities which may accept a huge number of possible values, the present value may be modified digit per digit. In most cases a possible range of values is predefined, which limits possible input values.

## Example: Modification of limit value 1 from 1.205 MW to 123.0 kW

| \% 1.205 w | >> Changeable value. Start modification via |
| :---: | :---: |
| LIMMI SMW W'FLLE | >> Description of the value on line 2 (ticker) |


| OK | First digit (1) starts flashing |
| :--- | :--- |
| $\Rightarrow$ | Second digit (2) starts flashing |
| $\Rightarrow$ | Third digit (0) starts flashing. Increase to 3 using |
| $\Rightarrow$ | Fourth digit (5) starts flashing. Decrease to 0 using |
| $\Rightarrow$ | M starts flashing. Reduce to $\mathbf{k}$ with one position after decimal point using |
| OK | 123.0 kW adopted as new limit value, displayed non flashing |

- The modification mode is left automatically after a time of 15 s with no key pressed and the previous displayed menu is shown again!
- The configuration mode is left automatically after a time of 30 s with no key pressed and the measurement display is shown again!


### 7.9 Data logger

The data logger offers a periodical acquisition of measurement data, such as recording load profiles, measurement fluctuations or meter readings as well as event triggered recordings of alarm states or distubances. This storage medium used is an SD card, which allows almost unlimited recordings and an easy exchanging on-site.

The following recording types are supported:

| Logger | Triggered by... | Recording | Resettable |
| :--- | :--- | :---: | :---: |
| Power mean values | Interval t1 | ON / OFF | YES |
| Configurable mean values quantities | Interval t2 | ON / OFF | YES |
| Extreme values | Interval t3 | ON / OFF | YES |
| Meter readings | Calendar based | ON / OFF | YES |
| Disturbance recorder | Event | ON / OFF | YES |
| Alarm / event list | Event | always active | NO |
| Operator list | Event | always active | NO |

### 7.9.1 Activation of data logger recording

By configuring the different data loggers their state will not be changed. If it was active it remains active, if it was inactive it remains inactive. The activation / deactivation of a specific logger may be performed via PC software or via the local programming menu. Only via PC software, respectively by using the corresponding commands via the configuration interface, contents of the individual logger can be reset. Lists are exceptional, because they are always active to prevent manipulations. They record events in endless mode and can't be reset.

### 7.9.2 SD card

The device is supplied with a 2 GByte SD card, which allows long-term recodings. The device can be equipped with all other SD cards available.


The red LED of the key located next to the SD card signalizes that the logger is active. During writing to the card the LED becomes dark for a short time.

To exchange an SD card the key must be pressed. As soon as the red LED becomes dark, the SD card can be removed and the new card inserted. Data can't be latched in the device. Therefore there is no recording for the time no card is present in the device.

| Status messages on LED display | Meaning |
| :---: | :---: |
| MU[梛召 | The logger is active, but no SD card has been inserted. |
| [ARTHILK | The SD card inserted is write-protected. |
|  | For at least one of the logger parts, which are not used in endless mode, the assigned memory space is full. No more data can be recorded. |
|  | Faulty SD card. Possibly no more data will be recorded. |

### 7.9.3 Access to logger data

Only for device versions with Ethernet a direct access tot he logger data via interface is possible. For all other versions you have to remove the SD card first and to access the recorded data using an internal or external card reader. The analysis of the data is performed using the supplied CB-Analyzer software.

### 7.9.4 Logger data analysis

The analysis of recorded logger data can be done using the supplied PC software CB-Analyzer. The software may also be downloaded free of charge from our homepage http://www.camillebauer.com .


The file "Read-me-first" on the Doku-CD provides all necessary information for the installation of the CB-Analyzer software and assistance for possible problems.

## Functionality of the CB-Analyzer software

This .NET-based software facilitates the data acquisition and analysis of the optional data loggers and lists of SINEAX CAM and Aplus. The data read from the devices will be stored in a database. The program is capable of processing several devices simultaneously.

- Acquisition of logger and list data of several devices
- Storage of the data in a database (Access, SQLClient)
- Different analyzing options of the acquired data, also across devices
- Report generation in list or graphic format
- Selectable time range in the preparation of reports
- Export of report data to Excel or as an Acrobat PDF file

The CB-Analyzer software provides a comprehensive help facility, which describes in detail the operation of the software. Below a screen-shot is shown, which shows as an example the graphical analysis of the power demand of a factory over one week.


## 8. Service, maintenance and disposal

### 8.1 Protection of data integrity

The ApLus supports security mechanism, which serve to prevent manipulation or undesired modifications of device data.

- Protection against device data modifications


### 8.2 Calibration and new adjustment

Each device is adjusted and checked before delivery. The condition as supplied to the customer is measured and stored in electronic form.

The uncertainty of measurement devices may be altered during normal operation if, for example, the specified ambient conditions are not met. If desired, in our factory a calibration can be performed, including a new adjustment if necessary, to assure the accuracy of the device.

### 8.3 Cleaning

The display and the operating keys should be cleaned in regular intervalls. Use a dry or slightly moist cloth for this.


## Damage due to detergents

Detergents may not only affect the clearness of the display but also can damage the device. Therefore, do not use detergents.

### 8.4 Battery

The device contains a battery for buffering the internal clock. It cannot be changed by the user. The replacement can be done at the factory only.

### 8.5 Disposal

The product must be disposed in compliance with local regulations. This particularly applies to the built-in battery.

## 9. Technical data

## Inputs

| Nominal current: | adjustable 1...5 A Current |
| :---: | :---: |
| Maximum: | 7.5 A (sinusoidal) Range: |
| Consumption: | $\leq I^{2} \times 0.01 \Omega$ per phase $\quad$ See oper |
| Overload capacity: | 10 A continuous <br> ACF30 |
|  | $100 \mathrm{~A}, 10 \times 1 \mathrm{~s}$, interval 100 s |
| Nominal voltage: | $57.7 \ldots 400 \mathrm{~V}_{\mathrm{LN}}, 100 \ldots 693 \mathrm{~V}_{\mathrm{LL}}$ |
| Maximum: | $480 \mathrm{~V}_{\text {LN }} 832 \mathrm{~V}_{\text {LL }}$ (sinusoidal) |
| Consumption: | $\leq \mathrm{U}^{2} / 3 \mathrm{M} \Omega$ per phase |
| Impedance: | $3 \mathrm{M} \Omega$ per phase |
| Overload capacity: | $480 \mathrm{~V}_{\text {LN }}, 832 \mathrm{~V}_{\text {LL }}$ continuous |
|  | $600 \mathrm{~V}_{\mathrm{LN}}, 1040 \mathrm{~V}_{\mathrm{LL}}, 10 \times 10 \mathrm{~s}$, interval 10s |
|  | $800 \mathrm{~V}_{\mathrm{LN}}, 1386 \mathrm{~V}_{\mathrm{LL}}, 10 \times 1 \mathrm{~s}$, interval 10s |
| Systems: | Single phase |
|  | Split phase (2-phase system) |
|  | 3-wire, balanced load |
|  | 3-wire, unbalanced load |
|  | 3 -wire, unbalanced load, Aron connection |
|  | 4-wire, balanced load |
|  | 4-wire, unbalanced load |
|  | 4-wire, unbalanced load, Open-Y |
| Nominal frequency: | 45... 50 / 60 ...65Hz |
| Measurement TRMS: | Up to the 63rd harmonic |

## Measurement uncertainty

1

## Version with Rogowski current inputs

The additional uncertainty of the Rogowski coils ACF 3000 is not included in the following specifications: See operating instructions of Rogowski coil ACF3000

Reference conditions: Ambient $15 \ldots 30^{\circ} \mathrm{C}$,
(acc. IEC/EN 60688) sinusoidal input signals (form factor 1.1107)
Measurement over 8 cycles, no fixed system frequency for sampling,
$P F=1$, frequency $50 \ldots 60 \mathrm{~Hz}$
Voltage, current: $\pm(0.08 \% M V+0.02 \% M R)^{1)}{ }^{2)}$
Power: $\quad \pm(0.16 \% M V+0.04 \% M R)^{3)}{ }^{2)}$
Power factor: $\left.\quad \pm 0.1^{\circ} 4\right)$
Frequency: $\quad \pm 0.01 \mathrm{~Hz}$
Imbalance U, I: $\quad \pm 0.5 \%$
Harmonics: $\pm 0.5 \%$
THD Voltage: $\quad \pm 0.5 \%$
TDD Current: $\quad \pm 0.5 \%$
Active energy: Class 0.5S, EN 62053-22
Reactive energy: Class 2, EN 62053-23
Measurement with fixed system frequency:
General $\pm$ Basic uncertainty $\times\left(F_{\text {konfig }}-F_{\text {ist }}\right)[H z] \times 10$
Imbalance U $\pm 1.5 \%$ up to $\pm 0.5 \mathrm{~Hz}$
Harmonics $\quad \pm 1.5 \%$ up to $\pm 0.5 \mathrm{~Hz}$
THD, TDD $\pm 2.0 \%$ up to $\pm 0.5 \mathrm{~Hz}$
${ }^{1)}$ MV: Measured value, MR: measurement range (maximum)
${ }^{2)}$ Additional uncertainty of $0.1 \%$ MV if neutral wire not connected (3-wire connections)
${ }^{3)}$ MR: maximum voltage $x$ maximum current
${ }^{4)}$ Additional uncertainty of $0.1^{\circ}$ if neutral wire not connected (3-wire connections)

## Zero suppression, range limitations

The measurement of specific quantities is related to a pre-condition which must be fulfilled, that the corresponding value can be determined and sent via interface or displayed. If this condition is not fulfilled, a default value is used for the measurement.

| Quantity | Condition | Default |
| :---: | :---: | :---: |
| Voltage | $U x<1 \% U x_{\text {max }}$ | 0.00 |
| Current | $\mathrm{lx}<0,1 \% \mathrm{IX}_{\text {max }}$ | 0.00 |
| PF | $S x<1 \% S x_{\text {max }}$ | 1.00 |
| QF, LF, $\tan \varphi$ | $S x<1 \% S x_{\text {max }}$ | 0.00 |
| Frequency | voltage and/or current input too low ${ }^{1)}$ | 44.90 |
| Voltage unbalance | $U x<5 \% \mathrm{Ux}_{\text {max }}$ | 0.00 |
| Current unbalance | mean value of phase currents $<5 \% 1 \mathrm{x}_{\max }$ | 0.00 |
| Phase angle | at least one voltage $U x<5 \% U x_{\text {max }}$ | $120^{\circ}$ |
| Harmonics U, THD-U | fundamental < $5 \%$ Ux ${ }_{\text {max }}$ | 0.00 |

${ }^{1)}$ specific level depends on the device configuration

| Power supply | via plug-in terminal |
| :--- | :--- |
| Nominal voltage: | $100 \ldots 230 \mathrm{~V} \mathrm{AC} \pm 15 \%, 50 \ldots 400 \mathrm{~Hz}$ |
|  | $24 \ldots . .230 \mathrm{~V}$ DC $\pm 15 \%$ |
|  | $\leq 7 \ldots 10 \mathrm{VA}$, depending on the device hardware used |

## I/O interface

Available inputs and outputs

| Basic unit | -1 relay output, changeover contact |
| :--- | :--- |
|  | -1 digital output (fixed) |
|  | -1 digital input (fixed) |
| I/O extension 1 | -2 relay outputs, changeover contact |
|  | -4 bipolar analog outputs |
|  | -2 digital inputs/outputs, each configurable as input or output |
| I/O extension 2 | -2 relay outputs, changeover contact |
|  | -6 digital inputs/outputs, each configurable as input or output |


| Analog outputs | via plug-in terminals, galvanically isolated |
| :---: | :---: |
| Linearization: | Linear, quadratic, kinked |
| Range: | $\pm 20 \mathrm{~mA}$ ( 24 mA max.), bipolar |
| Uncertainty: | $\pm 0.2 \%$ of 20 mA |
| Burden: | $\leq 500 \Omega(\mathrm{max} .10 \mathrm{~V} / 20 \mathrm{~mA})$ |
| Burden influence: | $\leq 0.2 \%$ |
| Residual ripple: | $\leq 0.4 \%$ |
| Response time: | $60 . . .100 \mathrm{~ms}$ (for 2 cycles averaging time of RMS values) |
| Relays | via plug-in terminals |
| Contact: | changeover contact, bistabil |
| Load capacity: | $250 \mathrm{~V} \mathrm{AC} 2 \mathrm{~A},, 500 \mathrm{VA}$ |
|  | 30 V DC, 2 A, 60 W |
| Digital inputs/outputs | via plug-in terminals |
| Digital inputs (acc. EN 61 | 31-2 DC 24 V type 3): |
| Nominal voltage | 12 / 24 V DC (30 V max.) |
| Logical ZERO | - 3 up to +5 V |
| Logical ONE | 8 up to 30 V |
| Digital outputs (partly acc | EN 61 131-2): |
| Nominal voltage | 12 / 24 V DC (30 V max.) |
| Nominal current | 50 mA ( $60 \mathrm{~mA} \mathrm{max}$. ) |
| Load capability | $400 \Omega \ldots 1 \mathrm{M} \Omega$ |

## Interfaces

| Modbus/RTU X4 I X8 | via plug-in terminals |
| :---: | :---: |
| Protocol: | Modbus RTU |
| Physics: | RS-485, max. 1200m (4000 ft) |
| Baud rate: | 2'400, 4'800, 9'600, 19'200, 38'400, 57'600, 115'200 Baud |
| Number of participants: | $\leq 32$ |
| Profibus $\mathrm{X8}$ | via 9-pin D-sub socket |
| Protocol: | Profibus DP |
| Physics: | RS-485, 100...1200m (depending on baud rate and cable type used) |
| Baud rate: | Automatic baud rate recognition (9.6kBit/s ... 12MBit/s) |
| Address: | 0... 125 (default: 126) |
| Ethernet X4 | via RJ45 connector |
| Protocol: | Modbus/TCP, NTP |
| Physics: | Ethernet 100BaseTX |
| Mode: | 10/100 MBit/s, full/half duplex, auto-negotiation |
| Internal clock (RTC) |  |
| Uncertainty: | $\pm 2$ minutes / month ( 15 up to $30^{\circ} \mathrm{C}$ ) |
| Synchronization: | via Synchronization pulse |
| Running reserve: | > 10 years |

## Ambient conditions, general information

Operating temperature: -10 up to 15 up to 30 up to $+55^{\circ} \mathrm{C}$
Storage temperature: $\quad-25$ up to $+70^{\circ} \mathrm{C}$
Temperature influence: $\quad 0.5 \times$ measurement uncertainty per 10 K
Long term drift: $\quad 0.2 \times$ measurement uncertainty per year
Others: Usage group II (EN 60 688)
Relative humidity: <95\% no condensation
Altitude: $\leq 2000$ m max.
Device to be used indoor only !

## Mechanical attributes

| Orientation: | Any |
| :--- | :--- |
| Housing material: | Polycarbonat (Makrolon) |
| Flammability class: | V-0 acc. UL94, non-dripping, free of halogen |
| Weight: | 500 g |
| Dimensions: | Dimensional drawings |

Vibration withstand (test according to DIN EN 60 068-2-6)

Acceleration:
Frequency range:
Number of cycles:
$\pm 5 \mathrm{~g}$
$10 \ldots 150 \ldots 10 \mathrm{~Hz}$, rate of frequency sweep: 1 octave/minute
10 in each of the 3 axes

## Security

The current inputs are galvanically isolated from each other
Protection class: II (protective insulation, voltage inputs via protective impedance)
Pollution degree: 2
Protection:
IP64 (front), IP40 (housing), IP20 (terminals)
Measurement category:
Rated voltage
(versus earth):
CAT III, CATII (relays)
power supply: 265 V AC
Relays: 250 V AC
I/O's: 30 V DC
Test voltages: DC, 1 min., acc. IEC/EN 61010-1
7504 V DC, power supply versus inputs U, I
5008 V DC, power supply versus bus, I/O's, relays
6030 V DC, inputs $U$ versus inputs I
4690V DC, inputs U after protective impedance versus bus, I/O's, relays
7504 V DC, inputs U versus relays
7504V DC, inputs I versus bus, I/O's, relays
6030V DC, inputs I versus inputs I
3130V DC, relay versus relay, bus, I/O's

## Applied regulations, standards and directives

IEC/EN 61 010-1
IEC/EN 60688

DIN 40110
IEC/EN 60 068-2-1/ -2/-3/-6/-27:

IEC/EN 60529
IEC/EN 61 000-6-2/
61 000-6-4:
IEC/EN 61 131-2

IEC/EN 61326

IEC/EN 62 053-31
UL94
2002/95/EG (RoHS)

Safety regulations for electrical measuring, control and laboratory equipment Electrical measuring transducers for converting AC electrical variables into analog or digital signals
AC quantities
Ambient tests
-1 Cold, -2 Dry heat, -3 Damp heat, -6 Vibration, -27 Shock
Protection type by case
Electromagnetic compatibility (EMC)
Generic standard for industrial environment
Programmable controllers - equipment, requirements and tests (digital inputs/outputs 12/24V DC)
Electrical equipment for measurement, control and laboratory use - EMC requirements

Pulse output devices for electromechanical and electronic meters (S0 output) Tests for flammability of plastic materials for parts in devices and appliances EC directive on the restriction of the use of certain hazardous substances

## Warning

This is a class A product. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

This device complies with part 15 of the FCC:
Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

This Class A digital apparatus complies with Canadian ICES-0003.
10. Dimensional drawings



ApLUS with display


Aplus without display

## Annex

## A Description of measured quantities

## Used abbreviations

1L Single phase system
2L Split phase; system with 2 phases and centre tap
3Lb 3-wire system with balanced load
3Lu 3-wire system with unbalanced load
3Lu.A 3 -wire system with unbalanced load, Aron connection (only 2 currents connected)
4Lb 4-wire system with balanced load
4Lu 4-wire system with unbalanced load
4Lu.O 4-wire system with unbalanced load, Open-Y (reduced voltage connection)

## A1 Basic measurements

These measured quantities are determined using the configured measurement time ( $2 \ldots .1024$ cycles, in steps of 2 cycles). If a measurement is available depends on the selected system.
Depending on the measured quantity also minimum and maximum values are determined and non-volatile stored with timestamp. These values may be reset by the user via the display unit or via the configuration interface, see resetting of measurements.

| Measurement | $\stackrel{\rightharpoonup}{0}$ <br> ¢ <br> ¢ <br> ¢ <br> 0 | 区 | . | - | N | $\frac{\mathrm{P}}{\mathrm{~m}}$ | בی | $\begin{aligned} & \mathbb{1} \\ & \underset{N}{5} \end{aligned}$ | $\frac{\varrho}{\ni}$ | O | ב |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Voltage U | $\bullet$ | - | $\bullet$ | $\checkmark$ | $\checkmark$ |  |  |  | $\checkmark$ |  |  |
| Voltage $\mathrm{U}_{1 \mathrm{~N}}$ | $\bullet$ | $\bullet$ | $\bullet$ |  | $\checkmark$ |  |  |  |  | $\checkmark$ | $\checkmark$ |
| Voltage $\mathrm{U}_{2 \mathrm{~N}}$ | $\bullet$ | - | - |  | $\checkmark$ |  |  |  |  | $\sqrt{ }$ | $\checkmark$ |
| Voltage $\mathrm{U}_{3 \mathrm{~N}}$ | $\bullet$ | - | $\bullet$ |  |  |  |  |  |  | $\sqrt{ }$ | $\sqrt{ }$ |
| Voltage $\mathrm{U}_{12}$ | $\bullet$ | $\bullet$ | $\bullet$ |  |  | $\checkmark$ | $\checkmark$ | $\checkmark$ |  | $\sqrt{ }$ | $\checkmark$ |
| Voltage $\mathrm{U}_{23}$ | $\bullet$ | - | $\bullet$ |  |  | $\checkmark$ | $\checkmark$ | $\checkmark$ |  | $\sqrt{ }$ | $\checkmark$ |
| Voltage $\mathrm{U}_{31}$ | $\bullet$ | $\bullet$ | $\bullet$ |  |  | $\checkmark$ | $\checkmark$ | $\checkmark$ |  | $\checkmark$ | $\checkmark$ |
| Zero displacement voltage $\mathrm{U}_{\mathrm{NE}}$ | $\bullet$ | $\bullet$ |  |  |  |  |  |  |  |  | $\checkmark$ |
| Current I | - | $\bullet$ |  | $\checkmark$ |  | $\checkmark$ |  |  | $\checkmark$ |  |  |
| Current I1 | $\bullet$ | $\bullet$ |  |  | $\checkmark$ |  | $\checkmark$ | $\checkmark$ |  | $\checkmark$ | $\checkmark$ |
| Current I2 | $\bullet$ | $\bullet$ |  |  | $\checkmark$ |  | $\checkmark$ | $\checkmark$ |  | $\sqrt{ }$ | $\checkmark$ |
| Current I3 | - | $\bullet$ |  |  |  |  | $\checkmark$ | $\checkmark$ |  | $\sqrt{ }$ | $\checkmark$ |
| Bimetal current 1...60min. IB | $\bullet$ | $\bullet$ |  | $\checkmark$ |  | $\checkmark$ |  |  | $\checkmark$ |  |  |
| Bimetal current 1...60min. IB1 | $\bullet$ | $\bullet$ |  |  | $\checkmark$ |  | $\checkmark$ | $\checkmark$ |  | $\checkmark$ | $\checkmark$ |
| Bimetal current 1...60min. IB2 | $\bullet$ | $\bullet$ |  |  | $\checkmark$ |  | $\checkmark$ | $\checkmark$ |  | $\checkmark$ | $\checkmark$ |
| Bimetal current 1...60min. IB3 | $\bullet$ | $\bullet$ |  |  |  |  | $\checkmark$ | $\checkmark$ |  | $\sqrt{ }$ | $\checkmark$ |
| Neutral current $\mathrm{I}_{\mathrm{N}}$ | $\bullet$ | $\bullet$ |  |  |  |  |  |  |  | $\checkmark$ | $\checkmark$ |
| Active power P | $\bullet$ | $\bullet$ |  | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Active power P1 | $\bullet$ | $\bullet$ |  |  | $\sqrt{ }$ |  |  |  |  | $\checkmark$ | $\checkmark$ |
| Active power P2 | $\bullet$ | $\bullet$ |  |  | $\checkmark$ |  |  |  |  | $\checkmark$ | $\checkmark$ |
| Active power P3 | $\bullet$ | $\bullet$ |  |  |  |  |  |  |  | $\sqrt{ }$ | $\checkmark$ |
| Reactive power Q | $\bullet$ | $\bullet$ |  | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\sqrt{ }$ | $\checkmark$ |
| Reactive power Q1 | $\bullet$ | $\bullet$ |  |  | $\checkmark$ |  |  |  |  | $\checkmark$ | $\checkmark$ |
| Reactive power Q2 | $\bullet$ | $\bullet$ |  |  | $\checkmark$ |  |  |  |  | $\sqrt{ }$ | $\checkmark$ |
| Reactive power Q3 | $\bullet$ | $\bullet$ |  |  |  |  |  |  |  | $\sqrt{ }$ | $\checkmark$ |
| Apparent power S | $\bullet$ | $\bullet$ |  | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\sqrt{ }$ | $\sqrt{ }$ | $\sqrt{ }$ | $\sqrt{ }$ |
| Apparent power S1 | $\bullet$ | $\bullet$ |  |  | $\checkmark$ |  |  |  |  | $\sqrt{ }$ | $\checkmark$ |
| Apparent power S2 | $\bullet$ | $\bullet$ |  |  | $\sqrt{ }$ |  |  |  |  | $\sqrt{ }$ | $\checkmark$ |
| Apparent power S3 | $\bullet$ | $\bullet$ |  |  |  |  |  |  |  | $\sqrt{ }$ | $\checkmark$ |
| Frequency F | $\bullet$ | $\bullet$ | - | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\sqrt{ }$ | $\checkmark$ |


| Measurement | $\stackrel{\rightharpoonup}{0}$ U ¢ ¢ | $\underset{\Xi}{\text { 厄 }}$ | $\cdots$ | － | N | 号 | בे | 岕 | 号 | $\xrightarrow{\text { O }}$ | $\stackrel{3}{7}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Power factor PF | $\bullet$ |  |  | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Power factor PF1 | $\bullet$ |  |  |  | $\checkmark$ |  |  |  |  | $\checkmark$ | $\checkmark$ |
| Power factor PF2 | $\bullet$ |  |  |  | $\checkmark$ |  |  |  |  | $\checkmark$ | $\checkmark$ |
| Power factor PF3 | $\bullet$ |  |  |  |  |  |  |  |  | $\checkmark$ | $\checkmark$ |
| PF incoming inductive |  |  | $\bullet$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| PF incoming capacitive |  |  | $\bullet$ | $\checkmark$ | $\checkmark$ | $\sqrt{ }$ | $\sqrt{ }$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\sqrt{ }$ |
| PF outgoing inductive |  |  | $\bullet$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| PF outgoing capacitive |  |  | $\bullet$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Reactive power factor QF | $\bullet$ |  |  | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Reactive power factor QF1 | $\bullet$ |  |  |  | $\checkmark$ |  |  |  |  | $\checkmark$ | $\checkmark$ |
| Reactive power factor QF2 | $\bullet$ |  |  |  | $\checkmark$ |  |  |  |  | $\checkmark$ | $\checkmark$ |
| Reactive power factor QF3 | $\bullet$ |  |  |  |  |  |  |  |  | $\checkmark$ | $\sqrt{ }$ |
| Load factor LF | $\bullet$ |  |  | $\sqrt{ }$ | $\checkmark$ | $\sqrt{ }$ | $\checkmark$ | $\checkmark$ | $\sqrt{ }$ | $\sqrt{ }$ | $\sqrt{ }$ |
| Load factor LF1 | $\bullet$ |  |  |  | $\checkmark$ |  |  |  |  | $\sqrt{ }$ | $\checkmark$ |
| Load factor LF2 | $\bullet$ |  |  |  | $\checkmark$ |  |  |  |  | $\checkmark$ | $\checkmark$ |
| Load factor LF3 | $\bullet$ |  |  |  |  |  |  |  |  | $\checkmark$ | $\checkmark$ |
| $\mathrm{U}_{\text {mean }}=(\mathrm{U} 1 \mathrm{~N}+\mathrm{U} 2 \mathrm{~N}) / 2$ | $\bullet$ |  |  |  | $\checkmark$ |  |  |  |  |  |  |
| $\mathrm{U}_{\text {mean }}=(\mathrm{U} 1 \mathrm{~N}+\mathrm{U} 2 \mathrm{~N}+\mathrm{U} 3 \mathrm{~N}) / 3$ | $\bullet$ |  |  |  |  |  |  |  |  | $\checkmark$ | $\checkmark$ |
| $\mathrm{U}_{\text {mean }}=(\mathrm{U} 12+\mathrm{U} 23+\mathrm{U} 3$ ）／3 | $\bullet$ |  |  |  |  |  | $\checkmark$ | $\checkmark$ |  |  |  |
| $\mathrm{I}_{\text {mean }}=(11+12) / 2$ | $\bullet$ |  |  |  | $\checkmark$ |  |  |  |  |  |  |
| $\mathrm{I}_{\text {mean }}=(11+12+13) / 3$ | $\bullet$ |  |  |  |  |  | $\checkmark$ | $\checkmark$ |  | $\checkmark$ | $\checkmark$ |
| Phase angle between U1 and U2 | $\bullet$ |  |  |  |  | $\checkmark$ | $\checkmark$ | $\checkmark$ |  | $\checkmark$ | $\checkmark$ |
| Phase angle between U2 and U3 | $\bullet$ |  |  |  |  | $\checkmark$ | $\checkmark$ | $\checkmark$ |  | $\checkmark$ | $\checkmark$ |
| Phase angle between U3 and U1 | $\bullet$ |  |  |  |  | $\checkmark$ | $\checkmark$ | $\checkmark$ |  | $\checkmark$ | $\checkmark$ |
| Maximum $\Delta \mathrm{U}$＜＞Um ${ }^{1)}$ | $\bullet$ | $\bullet$ |  |  |  | $\sqrt{ }$ | $\checkmark$ | $\checkmark$ |  |  | $\sqrt{ }$ |
| Maximum $\Delta \mathrm{l}$＜＞ $\mathrm{Im}^{2)}$ | $\bullet$ | $\bullet$ |  |  |  |  | $\sqrt{ }$ |  |  | $\sqrt{ }$ | $\sqrt{ }$ |
| IMS，Average current with sign of $P$ | $\bullet$ |  |  |  |  |  | $\sqrt{ }$ | $\checkmark$ |  | $\checkmark$ | $\sqrt{ }$ |

${ }^{1)}$ maximum deviation from the mean value of all voltages（see A3）
${ }^{2)}$ maximum deviation from the mean value of all currents（see A3）

## Power factors

The power factor PF gives the relation between active and apparent power．If there are no harmonics present in the system，it corresponds to the $\cos \varphi$（see also Reactive power）．The PF has a range of $-1 . . .0 . .+1$ ，where the sign gives the direction of energy flow．
The load factor LF is a quantity derived from the PF，which allows making a statement about the load type．Only this way it＇s possible to measure a range like 0.5 capacitive ．．． 1 ．．． 0.5 inductive in a non－ambiguous way．
The reactive power factor QF gives the
 relation between reactive and apparent power．

## Zero displacement voltage $\mathbf{U}_{\mathrm{NE}}$

Starting from the generating system with star point E (which is normally earthed), the star point ( N ) on load side is shifted in case of unbalanced load. The zero displacement voltage between E und N may be determined by a vectorial addition of the voltage vectors of the three phases:

$$
\underline{U}_{N E}=-\left(\underline{U}_{1 N}+\underline{U}_{2 N}+\underline{U}_{3 N}\right) / 3
$$

A displacement voltage may also occur due to harmonics of order $3,9,15,21$ etc., because the dedicated currents add in the neutral wire.


## Earth fault monitoring in IT systems

Via the determination of the zero displacement voltage it's possible to detect a first earth fault in an unearthed IT system. To do so, the device is configured for measurement in a 4-wire system with unbalanced load and the neutral connector is connected to earth. In case of a single phase earth fault there is a resulting zero displacement voltage of $U_{L L} / \sqrt{ } 3$. The alarming may be done e.g. by means of a relay output.


Because in case of a fault the voltage triangle formed by the three phases does not change the voltage and current measurements as well as the system power values will be still measured and displayed correctly. Also the meters carry on to work as expected.
The method is suited to detect a fault condition during normal operation. A declination of the isolation resistance may not be detected this way. This should be measured during a periodical control of the system using a mobile system.

Another possibility to analyze fault conditions in a grid offers the method of the symmetrical components as described in A3.

## A2 Harmonic analysis

| Measurement | \# | 즐 | - | N | - | د | 咅 | $\stackrel{\text { ? }}{7}$ | - | $\stackrel{3}{7}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| THD Voltage U1N/U | $\bullet$ | $\bullet$ | $\checkmark$ | $\sqrt{ }$ |  |  |  | $\sqrt{ }$ | $\sqrt{ }$ | $\sqrt{ }$ |
| THD Voltage U2N | $\bullet$ | $\bullet$ | $\sqrt{ }$ | $\sqrt{ }$ |  |  |  |  | $\sqrt{ }$ | $\sqrt{ }$ |
| THD Voltage U3N | $\bullet$ | $\bullet$ |  |  |  |  |  |  | $\checkmark$ | $\sqrt{ }$ |
| THD Voltage U12 | $\bullet$ | $\bullet$ |  |  | $\checkmark$ | $\sqrt{ }$ | $\checkmark$ |  |  |  |
| THD Voltage U23 | $\bullet$ | $\bullet$ |  |  | $\sqrt{ }$ | $\sqrt{ }$ | $\sqrt{ }$ |  |  |  |
| THD Voltage U31 | - | $\bullet$ |  |  | $\sqrt{ }$ | $\sqrt{ }$ | $\sqrt{ }$ |  |  |  |
| TDD Current I1/I | $\bullet$ | $\bullet$ | $\sqrt{ }$ | $\sqrt{ }$ | $\sqrt{ }$ | $\sqrt{ }$ | $\sqrt{ }$ | $\sqrt{ }$ | $\sqrt{ }$ | $\sqrt{ }$ |
| TDD Current I2 | - | $\bullet$ |  | $\sqrt{ }$ |  | $\sqrt{ }$ | $\sqrt{ }$ |  | $\sqrt{ }$ | $\sqrt{ }$ |
| TDD Current I3 | $\bullet$ | $\bullet$ |  |  |  | $\sqrt{ }$ | $\sqrt{ }$ |  | $\sqrt{ }$ | $\sqrt{ }$ |
| Harmonic contents 2nd...50th U1N/U | $\bullet$ | $\bullet$ | $\sqrt{ }$ | $\sqrt{ }$ |  |  |  | $\sqrt{ }$ | $\sqrt{ }$ | $\sqrt{ }$ |
| Harmonic contents 2nd...50th U2N | $\bullet$ | $\bullet$ |  | $\sqrt{ }$ |  |  |  |  | $\sqrt{ }$ | $\sqrt{ }$ |
| Harmonic contents 2nd...50th U3N | $\bullet$ | $\bullet$ |  |  |  |  |  |  | $\sqrt{ }$ | $\sqrt{ }$ |
| Harmonic contents 2nd...50th U12 | $\bullet$ | $\bullet$ |  |  | $\sqrt{ }$ | $\sqrt{ }$ | $\sqrt{ }$ |  |  |  |
| Harmonic contents 2nd...50th U23 | $\bullet$ | $\bullet$ |  |  | $\sqrt{ }$ | $\sqrt{ }$ | $\sqrt{ }$ |  |  |  |
| Harmonic contents 2nd...50th 2.-50. U31 | $\bullet$ | $\bullet$ |  |  | $\sqrt{ }$ | $\sqrt{ }$ | $\sqrt{ }$ |  |  |  |
| Harmonic contents 2nd...50th 2.-50. 11/I | $\bullet$ | $\bullet$ | $\sqrt{ }$ | $\sqrt{ }$ | $\sqrt{ }$ | $\sqrt{ }$ | $\sqrt{ }$ | $\sqrt{ }$ | $\checkmark$ | $\sqrt{ }$ |
| Harmonic contents 2nd...50th 2.-50. 12 | $\bullet$ | $\bullet$ |  | $\sqrt{ }$ |  | $\sqrt{ }$ | $\sqrt{ }$ |  | $\sqrt{ }$ | $\sqrt{ }$ |
| Harmonic contents 2nd...50th 2.-50. I3 | $\bullet$ | $\bullet$ |  |  |  | $\sqrt{ }$ | $\sqrt{ }$ |  | $\sqrt{ }$ | $\sqrt{ }$ |

## Harmonics

Harmonics are multiple of the fundamental resp. system frequency. They arise if non-linear loads, such as RPM regulated drives, rectifiers, thyristor controlled systems or fluorescent lamps are present in the power system. Thus undesired side effects occur, such as additional thermical stress to operational resources or electrical mains, which lead to an advanced aging or even damage. Also the reliability of sensitive loads can be affected and unexplainable disturbances may occur. In industrial networks the image of the harmonics gives good information about the kind of loads connected. See also:

## - Increase of reactive power due to harmonic currents

## TDD (Total Demand Distortion)

In the Aplus the complete harmonic content of the currents is shown as Total Demand Distortion, briefly TDD. This value is scaled to the rated current resp. rated power. Only this way it's possible to estimate the influence of the current harmonics on the connected equipment correctly.

## Maximum values

The maximum values of the harmonic analysis arise from the monitoring of THD and TDD. The maximum values of individual harmonics are not monitored separately, but are stored if a maximum value of THD or TDD is detected. The image of the maximum harmonics therefore always corresponds to the dedicated THD resp. TDD.

The accuracy of the harmonic analysis depends strongly on the quality of the current and voltage transformers possibly used. In the harmonics range transformers normally change both, the amplitude and the phase of the signals to measure. It's valid: The higher the frequency of the harmonic, the higher its damping resp. phase shift.

## A3 System imbalance

| Measured quantity |  | ¢ | - | - | N | 号 | ב | 垗 | $\stackrel{0}{7}$ | O | ב |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| UR1: Positive sequence [V] | $\bullet$ |  |  |  |  | $\sqrt{ }$ | $\sqrt{ }$ | $\sqrt{ }$ |  |  | $\checkmark$ |
| UR2: Negative sequence [V] | $\bullet$ |  |  |  |  | $\checkmark$ | $\sqrt{ }$ | $\sqrt{ }$ |  |  | $\checkmark$ |
| U0: Zero sequence [V] | $\bullet$ |  |  |  |  |  |  |  |  |  | $\checkmark$ |
| U: Imbalance UR2/UR1 | $\bullet$ | $\bullet$ |  |  |  | $\sqrt{ }$ | $\sqrt{ }$ | $\sqrt{ }$ |  |  | $\sqrt{ }$ |
| U: Imbalance U0/UR1 | $\bullet$ | $\bullet$ |  |  |  |  |  |  |  |  | $\checkmark$ |
| IR1: Positive sequence [A] | $\bullet$ |  |  |  |  |  | $\sqrt{ }$ |  |  | $\sqrt{ }$ | $\sqrt{ }$ |
| IR2: Negative sequence [A] | $\bullet$ |  |  |  |  |  | $\sqrt{ }$ |  |  | $\sqrt{ }$ | $\sqrt{ }$ |
| IO: Zero sequence [A] | $\bullet$ |  |  |  |  |  | $\sqrt{ }$ |  |  | $\sqrt{ }$ | $\sqrt{ }$ |
| I: Imbalance IR2/IR1 | $\bullet$ | $\bullet$ |  |  |  |  | $\sqrt{ }$ |  |  | $\sqrt{ }$ | $\sqrt{ }$ |
| I: Imbalance I0/IR1 | $\bullet$ | $\bullet$ |  |  |  |  | $\sqrt{ }$ |  |  | $\sqrt{ }$ | $\sqrt{ }$ |

Available via interface only
Imbalance in three-phase systems may occur due to single-phase loads, but also due to failures, such as e.g. the blowing of a fuse, an earth fault, a phase failure or an isolation defect. Also harmonics of the 3rd, 9th, 15th, 21st etc. order, which add in the neutral wire, may lead to imbalance. Operating resources dimensioned to rated values, such as three-phase generators, transformers or motors on load side, may be excessively stressed by imbalance. So a shorter life cycle, a damage or failure due to thermical stress can result. Therefore monitoring imbalance helps to reduce the costs for maintenance and extends the undisturbed operating time of the used resources.

Imbalance or unbalanced load relays use different measurement principles. One of them is the approach of the symmetrical components, the other one calculates the maximum deviation from the mean-value of the three phase values. The results of these methods are not equal and don't have the same intention. Both of these principles are implemented in the ApLUs.

## Symmetrical components (acc. Fortescue)

The imbalance calculation method by means of the symmetrical components is ambitious and intensive to calculate. The results may be used for disturbance analysis and for protection purposes in three-phase systems. The real existing system is divided in symmetrical system parts: A positive sequence, a negative sequence and (for systems with neutral conductor) a zero sequence system. The approach is easiest to understand for rotating machines. The positive sequence represents a positive rotating field, the negative sequence a negative (braking) rotating field with opposite sense of direction. Therefore the negative sequence prevents that the machine can generate the full turning moment. For e.g. generators the maximum permissible current imbalance is typically limited to a value of $8 \ldots . .12 \%$.

## Maximum deviation from the mean value

The calculation of the maximum deviation from the mean value of the phase currents resp. phase voltages gives the information if a grid or substation is imbalanced loaded. The results are independent of rated values and the present load situation. So a more symmetrical system can be aspired, e.g. by changing loads from one phase to another.
Also failure detection is possible. The capacitors used in compensation systems are wear parts, which fail quite often and then have to be replaced. When using three phase power capacitors all phases will be compensated equally which leads to almost identical currents flowing through the capacitors, if the system load is comparable. By monitoring the current imbalance it's then possible to estimate if a capacitor failure is present.
The maximum deviations are calculated in the same steps as the instantaneous values and therefore are arranged there (see A1).

## A4 Reactive power

| Measured quantity |  | 区 | $\underline{E}$ | - | N | $\stackrel{\sim}{1}$ | $\stackrel{3}{m}$ | ¢ | $\stackrel{7}{7}$ | $\stackrel{0}{3}$ | 3 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Distortion reactive power D | $\bullet$ | $\bullet$ |  | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\sqrt{ }$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Distortion reactive power D1 | $\bullet$ | $\bullet$ |  |  | $\checkmark$ |  |  |  |  | $\checkmark$ | $\checkmark$ |
| Distortion reactive power D2 | $\bullet$ | $\bullet$ |  |  | $\checkmark$ |  |  |  |  | $\checkmark$ | $\checkmark$ |
| Distortion reactive power D3 | $\bullet$ | $\bullet$ |  |  |  |  |  |  |  | $\checkmark$ | $\checkmark$ |
| Fundamental reactive power QG | $\bullet$ | $\bullet$ |  | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Fundamental reactive power QG1 | $\bullet$ | $\bullet$ |  |  | $\checkmark$ |  |  |  |  | $\checkmark$ | $\checkmark$ |
| Fundamental reactive power QG2 | $\bullet$ | $\bullet$ |  |  | $\checkmark$ |  |  |  |  | $\checkmark$ | $\checkmark$ |
| Fundamental reactive power QG3 | $\bullet$ | $\bullet$ |  |  |  |  |  |  |  | $\checkmark$ | $\checkmark$ |
| $\cos \varphi$ of fundamental | $\bullet$ |  | $\bullet$ | $\checkmark$ | $\sqrt{ }$ | $\sqrt{ }$ | $\sqrt{ }$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\sqrt{ }$ |
| $\cos \varphi$ of fundamental L1 | $\bullet$ |  | $\bullet$ |  | $\checkmark$ |  |  |  |  | $\checkmark$ | $\checkmark$ |
| $\cos \varphi$ of fundamental L2 | $\bullet$ |  | - |  | $\checkmark$ |  |  |  |  | $\checkmark$ | $\checkmark$ |
| $\cos \varphi$ of fundamental L3 | $\bullet$ |  | $\bullet$ |  |  |  |  |  |  | $\checkmark$ | $\checkmark$ |
| $\cos \varphi$ of fundamental, incoming inductive |  |  | $\bullet$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| $\cos \varphi$ of fundamental, incoming capacitive |  |  | $\bullet$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\sqrt{ }$ |
| $\cos \varphi$ of fundamental, outgoing inductive |  |  | $\bullet$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\sqrt{ }$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| $\cos \varphi$ of fundamental, outgoing capacitive |  |  | $\bullet$ | $\checkmark$ | $\checkmark$ | $\sqrt{ }$ | $\sqrt{ }$ | $\sqrt{ }$ | $\sqrt{ }$ | $\checkmark$ | $\checkmark$ |
| $\tan \varphi$ of fundamental | $\bullet$ |  |  | $\checkmark$ | $\checkmark$ | $\sqrt{ }$ | $\checkmark$ | $\sqrt{ }$ | $\sqrt{ }$ | $\checkmark$ | $\sqrt{ }$ |
| $\tan \varphi$ of fundamental L1 | $\bullet$ |  |  |  | $\checkmark$ |  |  |  |  | $\checkmark$ | $\checkmark$ |
| $\tan \varphi$ of fundamental L2 | $\bullet$ |  |  |  | $\checkmark$ |  |  |  |  | $\checkmark$ | $\checkmark$ |
| $\tan \varphi$ of fundamental L3 | $\bullet$ |  |  |  |  |  |  |  |  | $\checkmark$ | $\checkmark$ |

Available via interface only
Most of the loads consume a combination of ohmic and inductive current from the power system. Reactive power arises by means of the inductive load. But the number of non-linear loads, such as RPM regulated drives, rectifiers, thyristor controlled systems or fluorescent lamps, is increasing. They cause nonsinusoidal AC currents, which may be represented as a sum of harmonics. Thus the reactive power to transmit increases and leads to higher transmission losses und higher energy costs. This part of the reactive power is called distortion reactive power.

Normally reactive power is unwanted, because there is no usable active component in it. Because the transmission of reactive power over long distances is uneconomic, it makes sense to install compensation systems close to the consumers. So transmission capacities may be used better and losses and voltage drops by means of harmonic currents can be avoided.


P: Active power
S: Apparent power including harmonic components

D S1: Fundamental apparent power
Q: Total reactive power
QG: Fundamental reactive power
D: Distortion reactive power

The reactive power may be divided in a fundamental and a distortion component. Only the fundamental reactive power may be compensated directly by means of the classical capacitive method. The distortion components have to be combated using inductors or active harmonic conditioners.

The Aplus reports a load factor PF which is the relation between active power P and apparent power S , including all possibly existing harmonic parts. This factor is often called $\cos \varphi$, which is only partly correct. The PF corresponds to the $\cos \varphi$ only, if there is no harmonic content present in the system. So the $\cos \varphi$ represents the relation between the active power P and the fundamental apparent power S .

Also calculated is the $\tan \varphi$, which is especially known as a target quantity for the reactive power compensative using capacitors. It corresponds to the relation of the fundamental reactive power QG and the active power P . Here intentionally the fundamental reactive power is used for the calculation, because this is the only component which may be directly compensated via capacitors.

## A5 Mean values and trend

| Measured quantity |  |  | 흧 ¢ | 莹 | 気 |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Active power incoming | 1s...60min. ${ }^{1)}$ | $\bullet$ | - | $\bullet$ | $\bullet$ | 5 |
| Active power outgoing | 1s...60min. ${ }^{1)}$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | 5 |
| Reactive power incoming | 1s...60min. ${ }^{1)}$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | 5 |
| Reactive power outgoing | 1s...60min. ${ }^{1)}$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | 5 |
| Reactive power inductive | 1s...60min. ${ }^{1)}$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | 5 |
| Reactive power capacitive | 1s...60min. ${ }^{1)}$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | 5 |
| Apparent power | 1s...60min. ${ }^{1)}$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | 5 |
| Mean value quantity 1 | 1s...60min. ${ }^{\text {2) }}$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | 1 |
| Mean value quantity 2 | 1s...60min. ${ }^{\text {2) }}$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | 1 |
| Mean value quantity 3 | 1s...60min. ${ }^{\text {2) }}$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | 1 |
| Mean value quantity 4 | 1s...60min. ${ }^{\text {2) }}$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | 1 |
| Mean value quantity 5 | 1s...60min. ${ }^{\text {2) }}$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | 1 |
| Mean value quantity 6 | 1s...60min. ${ }^{\text {2) }}$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | 1 |
| Mean value quantity 7 | 1s...60min. ${ }^{\text {2) }}$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | 1 |
| Mean value quantity 8 | 1s...60min. ${ }^{\text {2) }}$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | 1 |
| Mean value quantity 9 | 1s...60min. ${ }^{\text {2) }}$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | 1 |
| Mean value quantity 10 | 1s...60min. ${ }^{\text {2) }}$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | 1 |
| Mean value quantity 11 | 1s...60min. ${ }^{\text {2) }}$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | 1 |
| Mean value quantity 12 | 1s...60min. ${ }^{\text {2) }}$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | 1 |

Available via interface only ${ }^{1)}$ Interval time t1 ${ }^{2)}$ Interval time t2
The device calculates automatically the mean values of all system power quantities. In addition up to 12 further mean value quantities can be freely selected.

## Calculating the mean-values

The mean value calculation is performed via integration of the measured instantaneous values over a configurable averaging interval. The interval time may be selected in the range from one second up to one hour. Possible interim values are set the way that a multiple of it is equal to a minute or an hour. Mean values of power quantities (interval time t1) and free quantities (interval time t2) may have different averaging intervals.

## Synchronization

For the synchronization of the averaging intervals the internal clock or an external signal via digital input may be used. In case of an external synchronization the interval should be within the given range of one second up to one hour. The synchronization is important for making e.g. the mean value of power quantities on generating and demand side comparable.

## Trend

The estimated final value (trend) of mean values is determined by weighted addition of measurements of the past and the present interval. It serves for early detection of a possible exceeding of a given maximum value. This can then be avoided, e.g. by switching off an active load.

## History

For mean values of system powers the last 5 interval values may be displayed on the device or read via interface. For configurable quantities the value of the last interval is provided via communication interface.

## A6 Meters

| Measured quantity |  | － | N | 号 | ב | 訔 | 号 | O | 3 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Active energy incoming， | high tariff | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | － |
| Active energy outgoing， | high tariff | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ |
| Reactive energy inductive， | high tariff | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ |
| Reactive energy capacitive， | high tariff | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ |
| Reactive energy incoming， | high tariff | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | － |
| Reactive energy outgoing， | high tariff | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ |
| Active energy incoming， | low tariff | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ |
| Active energy outgoing， | low tariff | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ |
| Reactive energy inductive， | low tariff | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ |
| Reactive energy capacitive， | low tariff | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | － |
| Reactive energy incoming， | low tariff | $\bullet$ | $\bullet$ | － | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | － |
| Reactive energy outgoing， | low tariff | $\bullet$ | $\bullet$ | $\bullet$ | $\bullet$ | － | $\bullet$ | $\bullet$ | $\bullet$ |
| Active energy incoming L1， | high tariff |  | $\bullet$ |  |  |  |  | $\bullet$ | － |
| Active energy incoming L2， | high tariff |  | $\bullet$ |  |  |  |  | $\bullet$ | $\bullet$ |
| Active energy incoming L3， | high tariff |  |  |  |  |  |  | $\bullet$ | － |
| Reactive energy incoming L1， | high tariff |  | $\bullet$ |  |  |  |  | $\bullet$ | $\bullet$ |
| Reactive energy incoming L2， | high tariff |  | $\bullet$ |  |  |  |  | $\bullet$ | － |
| Reactive energy incoming L3， | high tariff |  |  |  |  |  |  | $\bullet$ | $\bullet$ |
| Active energy incoming L1， | low tariff |  | $\bullet$ |  |  |  |  | $\bullet$ | － |
| Active energy incoming L2， | low tariff |  | $\bullet$ |  |  |  |  | $\bullet$ | $\bullet$ |
| Active energy incoming L3， | low tariff |  |  |  |  |  |  | $\bullet$ | $\bullet$ |
| Reactive energy incoming L1， | low tariff |  | － |  |  |  |  | $\bullet$ | $\bullet$ |
| Reactive energy incoming L2， | low tariff |  | $\bullet$ |  |  |  |  | $\bullet$ | $\bullet$ |
| Reactive energy incoming L3， | low tariff |  |  |  |  |  |  | $\bullet$ | － |
| Meter I／O 2， | high tariff |  | Independent of measured system |  |  |  |  |  |  |
| Meter I／O 6， | high tariff |  |  |  |  |  |  |  |  |
| Meter I／O 7， | high tariff |  |  |  |  |  |  |  |  |
| Meter I／O 8， | high tariff |  |  |  |  |  |  |  |  |
| Meter I／O 9， | high tariff |  |  |  |  |  |  |  |  |
| Meter I／O 10， | high tariff |  |  |  |  |  |  |  |  |
| Meter I／O 11， | high tariff |  |  |  |  |  |  |  |  |
| Meter I／O 2， | low tariff |  |  |  |  |  |  |  |  |
| Meter I／O 6， | low tariff |  |  |  |  |  |  |  |  |
| Meter I／O 7， | low tariff |  |  |  |  |  |  |  |  |
| Meter I／O 8， | low tariff |  |  |  |  |  |  |  |  |
| Meter I／O 9， | low tariff |  |  |  |  |  |  |  |  |
| Meter I／O 10， | low tariff |  |  |  |  |  |  |  |  |
| Meter I／O 11， | low tariff |  |  |  |  |  |  |  |  |

## Standard meters

The meters for active and reactive energy of the system are always active．The meters for active and reactive energy demand per phase are active only，if the measured system is a multiple phase system with unbalanced load，otherwise they are removed from the above list．

## Meter reading on the display

## I／O meters

The meters of the I／O＇s are available only if the appropriate I／O＇s are configured as digital inputs for pulse counting，otherwise they are removed from the above list．No specific unit is shown for this kind of meters， because any energy form may be recorded here．

## B Display matrices in FULL mode

The fourth line of each image is allocated to a programmable meter value，which does not change even if another measurement image is selected．In the subsequent matrices，arranged in accordance with the measured system，this fourth line is not included．

B0 Used abbreviations for the measurements

| No． | Name | Description | Name（Display） |
| :---: | :---: | :---: | :---: |
| 0 | －－－ | not used | －－－ |
| 1 | U | Voltage system in single，3－or 4－wire systems | 4 |
| 2 | U1N | Voltage between phase L1 and neutral |  |
| 3 | U2N | Voltage between phase L2 and neutral | $\cup 2 n$ |
| 4 | U3N | Voltage between phase L3 and neutral | $\checkmark$ リn |
| 5 | U12 | Voltage between phases L1 and L2 | 412 |
| 6 | U23 | Voltage between phases L2 and L3 | $\checkmark 23$ |
| 7 | U31 | Voltage between phases L3 and L1 | $\sqcup 31$ |
| 8 | UNE | Zero displacement voltage 4－wire systems | $\sqcup \cap E$ |
| 9 | 1 | Current system in single，3－or 4－wire systems | 1 |
| 10 | 11 | Current phase L1 | 11 |
| 11 | 12 | Current phase L2 | 12 |
| 12 | 13 | Current phase L3 | 13 |
| 13 | IN | Neutral current | $1 \times$ |
| 14 | IB | Current damped，balanced system（bimetal） | 16 |
| 15 | IB1 | Current damped phase L1（bimetal） | 161 |
| 16 | IB2 | Current damped phase L2（bimetal） | 162 |
| 17 | IB3 | Current damped phase L3（bimetal） | 163 |
| 18 | P | Active power system（P＝P1＋P2＋P3） | $P$ |
| 19 | P1 | Active power phase L1 | $P 1$ |
| 20 | P2 | Active power phase L2 | P 2 |
| 21 | P3 | Active power phase L3 | P $\exists$ |
| 22 | Q | Reactive power system（Q＝Q1＋Q2＋Q3） | 9 |
| 23 | Q1 | Reactive power phase L1 | 91 |
| 24 | Q2 | Reactive power phase L2 | 92 |
| 25 | Q3 | Reactive power phase L3 | 93 |
| 26 | S | Apparent power system | 5 |
| 27 | S1 | Apparent power phase L1 | 51 |
| 28 | S2 | Apparent power phase L2 | 52 |
| 29 | S3 | Apparent power phase L3 | 53 |
| 30 | F | System frequency | $F$ |
| 31 | PF | Active power factor P／S，system | $P F$ |
| 32 | PF1 | Active power factor P1／S1，phase 1 | PF 1 |
| 33 | PF2 | Active power factor P2／S2，phase 2 | PF 2 |
| 34 | PF3 | Active power factor P3／S3，phase 3 | PF 3 |
| 35 | QF | Reactive power factor P／S，system | 9F |
| 36 | QF1 | Reactive power factor P1／S1，phase 1 | 9F 1 |
| 37 | QF2 | Reactive power factor P2／S2，phase 2 | 9F 2 |
| 38 | QF3 | Reactive power factor P3／S3，phase 3 | 9F 3 |
| 39 | LF | Load factor system，sign（Q）$\times(1-\mathrm{abs}(\mathrm{PF})$ | $L F$ |
| 40 | LF1 | Load factor phase L1 | $L F I$ |
| 41 | LF2 | Load factor phase L2 | $L F \geq$ |
| 42 | LF3 | Load factor phase L3 | LF 3 |
| 43 | U＿MEAN | Average voltage（U1N＋U2N＋U3N）／3 | 二ERn |
| 44 | I＿MEAN | Average current（11＋I2＋I3）／3 | 二ERn |
| 45 | UF12 | Phase angle U1－U2 | RU12 |
| 46 | UF23 | Phase angle U2－U3 | R． ²3 $^{\text {a }}$ |


| No. | Name | Description | Name (Display) |
| :---: | :---: | :---: | :---: |
| 47 | UF31 | Phase angle U3-U1 | R.Lヨ 1 |
| 48 | DEV_UMAX | Max. deviation from average of voltages | dEuL |
| 49 | DEV_IMAX | Max. deviation from average of currents | $d E_{u}$ |
| 50 | DEV_U1 | U1: deviation from average of voltages | dEuL |
| 51 | DEV_U2 | U2: deviation from average of voltages | dEuL |
| 52 | DEV_U3 | U3: deviation from average of voltages | dEu U |
| 53 | DEV_I1 | I1: deviation from average of currents | $d E_{u}$ |
| 54 | DEV_12 | 12: deviation from average of currents | dEul |
| 55 | DEV_I3 | I3: deviation from average of currents | $d E_{u}{ }^{\prime}$ |
| 56 | U_MAX | Maximum value of $U$ | $U$ |
| 57 | U1N_MAX | Maximum value of U1N | 4 in |
| 58 | U2N_MAX | Maximum value of U2N | $\checkmark$ 2n |
| 59 | U3N_MAX | Maximum value of U3N | $\checkmark \exists \pi$ |
| 60 | U12_MAX | Maximum value of U12 | 412 |
| 61 | U23_MAX | Maximum value of U23 | 423 |
| 62 | U31_MAX | Maximum value of U31 | $\cup 31$ |
| 63 | UNE_MAX | Maximum value of UNE | $\sqcup \cap E$ |
| 64 | I_MAX | Maximum value of I | 1 |
| 65 | I1_MAX | Maximum value of I1 | 11 |
| 66 | 12_MAX | Maximum value of 12 | 12 |
| 67 | I3_MAX | Maximum value of I3 | 13 |
| 68 | IN_MAX | Maximum value of IN | $1 \pi$ |
| 69 | IB_MAX | Maximum value of IB | 16 |
| 70 | IB1_MAX | Maximum value of IB1 | 161 |
| 71 | IB2_MAX | Maximum value of IB2 | 162 |
| 72 | IB3_MAX | Maximum value of IB3 | 163 |
| 73 | P_MAX | Maximum value of $P$ | $P$ |
| 74 | P1_MAX | Maximum value of P1 | $P 1$ |
| 75 | P2_MAX | Maximum value of P2 | P 2 |
| 76 | P3_MAX | Maximum value of P3 | P 3 |
| 77 | Q_MAX | Maximum value of Q | 9 |
| 78 | Q1_MAX | Maximum value of Q1 | 91 |
| 79 | Q2_MAX | Maximum value of Q2 | 9 2 |
| 80 | Q3_MAX | Maximum value of Q3 | 93 |
| 81 | S_MAX | Maximum value of S | 5 |
| 82 | S1_MAX | Maximum value of S1 | 51 |
| 83 | S2_MAX | Maximum value of S2 | 52 |
| 84 | S3_MAX | Maximum value of S3 | 53 |
| 85 | F_MAX | Maximum value of $F$ | $F$ |
| 86 | DEV_UMAX_MAX | Maximum value of DEV_UMAX | $d E \sim U$ |
| 87 | DEV_IMAX_MAX | Maximum value of DEV_IMAX | $d E_{u}$ |
| 88 | U_MIN | Minimum value of $U$ | $\sqcup$ |
| 89 | U1N_MIN | Minimum value of U1N | 4 ln |
| 90 | U2N_MIN | Minimum value of U2N | U 2n |
| 91 | U3N_MIN | Minimum value of U3N | $\checkmark$ ヨn |
| 92 | U12_MIN | Minimum value of U12 | 412 |
| 93 | U23_MIN | Minimum value of U23 | 423 |
| 94 | U31_MIN | Minimum value of U31 | $\cup 31$ |
| 95 | PF_MIN_IN_L | Minimum active power factor, incoming/inductive | PF. IL |
| 96 | PF_MIN_IN_C | Minimum active power factor, incoming/capacitive | PF. IL |
| 97 | PF_MIN_OUT_L | Minimum active power factor, outgoing/inductive | PF.aL |
| 98 | PF_MIN_OUT_C | Minimum active power factor, outgoing/capacitive | PF.aL |
| 99 | F_MIN | Minimum value of $f$ | $F$ |
| 100 | PIN | P incoming | $P$ in |


| No． | Name | Description | Name（Display） |
| :---: | :---: | :---: | :---: |
| 101 | P1IN | P1 incoming | $P$ ¢ 1 |
| 102 | P2IN | P2 incoming | $P$ ¢ 2 |
| 103 | P3IN | P3 incoming | $P \operatorname{RO}$ |
| 104 | POUT | P outgoing | Patt |
| 105 | P10UT | P1 outgoing | Patt |
| 106 | P2OUT | P2 outgoing | P |
| 107 | P3OUT | P3 outgoing | Patt |
| 108 | PIN＿OUT | P incoming－outgoing |  |
| 109 | P1IN＿OUT | P1 incoming－outgoing | $P \ln$ D |
| 110 | P2IN＿OUT | P2 incoming－outgoing | $P \operatorname{lnD}$ |
| 111 | P3IN＿OUT | P3 incoming－outgoing | $P$ ind |
| 112 | QIND | Q inductive | 9 ind |
| 113 | Q1IND | Q1 inductive | 9 ind |
| 114 | Q2IND | Q2 inductive | 9 ind |
| 115 | Q3IND | Q3 inductive | 9 ind |
| 116 | QCAP | Q capacitive | 9cRP |
| 117 | Q1CAP | Q1 capacitive | qсRP |
| 118 | Q2CAP | Q2 capacitive | qсRP |
| 119 | Q3CAP | Q3 capacitive | QcRP |
| 120 | QIN | Q incoming | ¢ 1 п |
| 121 | Q1IN | Q1 incoming | 9 in |
| 122 | Q2IN | Q2 incoming | 910 |
| 123 | Q3IN | Q3 incoming | 9 \％ |
| 124 | QOUT | Q outgoing | 9alt |
| 125 | Q10UT | Q1 outgoing | 9aUt |
| 126 | Q2OUT | Q2 outgoing | 9aUt |
| 127 | Q3OUT | Q3 outgoing | 9aUt |
| 128 | QIN＿OUT | Q incoming－outgoing | 9 ind |
| 129 | Q1IN＿OUT | Q1 incoming－outgoing | 9 in口 |
| 130 | Q2IN＿OUT | Q2 incoming－outgoing | 9 inD |
| 131 | Q3IN＿OUT | Q3 incoming－outgoing | 9 ind |
| 132 | UR1 | Positive sequence voltage | Ur 1 |
| 133 | UR2 | Negative sequence voltage | Urz |
| 134 | U0 | Zero sequence voltage | Uロ |
| 135 | IR1 | Positive sequence current | $1 r 1$ |
| 136 | IR2 | Negative sequence current | $1 r 2$ |
| 137 | 10 | Zero sequence current | 10 |
| 138 | UNB＿UR2＿UR1 | Unbalance factor voltage UR2／UR1 | Ur己 1 |
| 139 | UNB＿IR2＿IR1 | Unbalance factor current IR2／IR1 | $\mid r 己 1$ |
| 140 | UNB＿U0＿UR1 | Unbalance factor voltage U0／UR1 | Ural |
| 141 | UNB＿I0＿IR1 | Unbalance factor current I0／IR1 | ｜ral |
| 142 | THD＿U | Total Harmonic Distortion of U | thdu |
| 143 | THD＿U1N | Total Harmonic Distortion of U1N | thdU |
| 144 | THD＿U2N | Total Harmonic Distortion of U2N | thdu |
| 145 | THD＿U3N | Total Harmonic Distortion of U3N | thdu |
| 146 | THD＿U12 | Total Harmonic Distortion of U12 | thdU |
| 147 | THD＿U23 | Total Harmonic Distortion of U23 | thdu |
| 148 | THD＿U31 | Total Harmonic Distortion of U31 | thdu |
| 149 | TDD＿I | Total Demand Distortion of I | tdd．l |
| 150 | TDD＿I1 | Total Demand Distortion of I1 | tddl |
| 151 | TDD＿12 | Total Demand Distortion of I2 | tdd．l |
| 152 | TDD＿I3 | Total Demand Distortion of I3 | tdd．l |
| 153 | D | Distortion reactive power system | $d$ |
| 154 | D1 | Distortion reactive power phase L1 | d 1 |


| No． | Name | Description | Name（Display） |
| :---: | :---: | :---: | :---: |
| 155 | D2 | Distortion reactive power phase L2 | d2 |
| 156 | D3 | Distortion reactive power phase L3 | d3 |
| 157 | QG | Reactive power fundamental system | 9 HI |
| 158 | QG1 | Reactive power fundamental phase L1 | 9 HI |
| 159 | QG2 | Reactive power fundamental phase L2 | 9 H I |
| 160 | QG3 | Reactive power fundamental phase L3 | 9 HI |
| 161 | PFG | $\cos (\varphi)$ of fundamental system | cPh， |
| 162 | PFG1 | $\cos (\varphi)$ of fundamental phase L1 | cPh， |
| 163 | PFG2 | $\cos (\varphi)$ of fundamental phase L2 | cPh ， |
| 164 | PFG3 | $\cos (\varphi)$ of fundamental phase L3 | cPh ， |
| 161 | TG | $\tan (\varphi)$ of fundamental system | tPh， |
| 162 | TG1 | $\tan (\varphi)$ of fundamental phase L1 | tPh ， |
| 163 | TG2 | $\tan (\varphi)$ of fundamental phase L2 | $t P h$ ， |
| 164 | TG3 | $\tan (\varphi)$ of fundamental phase L3 | tPh ， |
| 169 | UNB＿UR2＿UR1＿MAX | Max．unbalance factor voltage UR2／UR1 | Urel |
| 170 | UNB＿IR2＿IR1＿MAX | Max．unbalance factor current IR2／IR1 | $\mid r 己 1$ |
| 171 | UNB＿U0＿UR1＿MAX | Max．unbalance factor voltage U0／UR1 | UrQ 1 |
| 172 | UNB＿IO＿IR1＿MAX | Max．unbalance factor current IO／IR1 | ｜ral |
| 173 | THD＿U＿MAX | Max．Total Harmonic Distortion of U | thdul |
| 174 | THD＿U1N＿MAX | Max．Total Harmonic Distortion of U1N | thdul |
| 175 | THD＿U2N＿MAX | Max．Total Harmonic Distortion of U2N | thdu |
| 176 | THD＿U3N＿MAX | Max．Total Harmonic Distortion of U3N | thdu |
| 177 | THD＿U12＿MAX | Max．Total Harmonic Distortion of U12 | thdU |
| 178 | THD＿U23＿MAX | Total Harmonic Distortion of U23 | thdU |
| 179 | THD＿U31＿MAX | Max．Total Harmonic Distortion of U31 | thdU |
| 180 | TDD＿I＿MAX | Max．Total Demand Distortion of I | tddl |
| 181 | TDD＿I1＿MAX | Max．Total Demand Distortion of I1 | tddl |
| 182 | TDD＿I2＿MAX | Max．Total Demand Distortion of I2 | tdd．l |
| 183 | TDD＿I3＿MAX | Max．Total Demand Distortion of I3 | tdd．l |
| 184 | D＿MAX | Max．distortion reactive power system | $d$ |
| 185 | D1＿MAX | Max．distortion reactive power phase L1 | d 1 |
| 186 | D2＿MAX | Max．distortion reactive power phase L2 | $d 己$ |
| 187 | D3＿MAX | Max．distortion reactive power phase L3 | dヨ |
| 188 | QG＿MAX | Max．reactive power fundamental system | 9 HI |
| 189 | QG1＿MAX | Max．reactive power fundamental phase L1 | 9 HI |
| 190 | QG2＿MAX | Max．reactive power fundamental phase L2 | 9 H I |
| 191 | QG3＿MAX | Max．reactive power fundamental phase L3 | 9 H I |
| 192 | PFG＿MIN＿IN＿L | Min． $\cos (\varphi)$ fundamental，incoming／inductive | cP．L |
| 193 | PFG＿MIN＿IN＿C | Min． $\cos (\varphi)$ fundamental，incoming／capacitive | cP．ıc |
| 194 | PFG＿MIN＿OUT＿L | Min． $\cos (\varphi)$ fundamental，outgoing／inductive | cP．ol |
| 195 | PFG＿MIN＿OUT＿C | Min． $\cos (\varphi)$ fundamental，outgoing／capacitive | сP．oc |
| 196 | M1＿PIN | Mean－value 1： P incoming（last interval） | P．inc |
| 197 | M2＿PIN | Mean－value 2：$P$ incoming（interval t－1） | P．inc |
| 198 | M3＿PIN | Mean－value 3：$P$ incoming（interval t－2） | P．inc |
| 199 | M4＿PIN | Mean－value 4： P incoming（interval $\mathrm{t}-3$ ） | P．inc |
| 200 | M5＿PIN | Mean－value 5： P incoming（interval t－4） | P．inc |
| 201 | M1＿POUT | Mean－value 1：P outgoing（last interval） | P．olt |
| 202 | M2＿POUT | Mean－value 2：P outgoing（interval t－1） | P．aUt |
| 203 | M3＿POUT | Mean－value 3：P outgoing（interval t－2） | P．alt |
| 204 | M4＿POUT | Mean－value 4：P outgoing（interval t－3） | P．alt |
| 205 | M5＿POUT | Mean－value 5：P outgoing（interval t－4） | P．aUt |
| 206 | M1＿QIN | Mean－value 1：Q incoming（last interval） | 9．inc |
| 207 | M2＿QIN | Mean－value 2：Q incoming（interval t－1） | 9．inc |
| 208 | M3＿QIN | Mean－value 3： Q incoming（interval t－2） | १．inc |


| No. | Name | Description | Name (Display) |
| :---: | :---: | :---: | :---: |
| 209 | M4_QIN | Mean-value 4: Q incoming (interval t-3) | 9. inc |
| 210 | M5_QIN | Mean-value 5: Q incoming (interval t-4) | १. $1 \cap$ |
| 211 | M1_QCAP | Mean-value 1: Q capacitive (last interval) | 9.cRP |
| 212 | M2_QCAP | Mean-value 2: Q capacitive (interval t-1) | Q.cRP |
| 213 | M3_QCAP | Mean-value 3: Q capacitive (interval t-2) | 9.cRP |
| 214 | M4_QCAP | Mean-value 4: Q capacitive (interval t-3) | 9.c RP |
| 215 | M5_QCAP | Mean-value 5: Q capacitive (interval t-4) | 9.cRP |
| 216 | M1_QIND | Mean-value 1: Q inductive (last interval) | Q. ind |
| 217 | M2_QIND | Mean-value 2: Q inductive (interval t-1) | Q. ind |
| 218 | M3_QIND | Mean-value 3: Q inductive (interval t-2) | 9. ind |
| 219 | M4_QIND | Mean-value 4: Q inductive (interval t-3) | Q. ind |
| 220 | M5_QIND | Mean-value 5: Q inductive (interval t-4) | 9. ind |
| 221 | M1_QOUT | Mean-value 1: Q outgoing (last interval) | 9.att |
| 222 | M2_QOUT | Mean-value 2: Q outgoing (interval t-1) | 9.aUt |
| 223 | M3_QOUT | Mean-value 3: Q outgoing (interval t-2) | 9.att |
| 224 | M4_QOUT | Mean-value 4: Q outgoing (interval t-3) | 9.aUt |
| 225 | M5_QOUT | Mean-value 5: Q outgoing (interval t-4) | 9.aUt |
| 226 | M1_S | Mean-value 1: S (last interval) | 5 |
| 227 | M2_S | Mean-value 2: S (interval t-1) | 5 |
| 228 | M3_S | Mean-value 3: S (interval t-2) | 5 |
| 229 | M4_S | Mean-value 4: S (interval t-3) | 5 |
| 230 | M5_S | Mean-value 5: S (interval t-4) | 5 |
| 231 | TR_PIN | Trend mean-value P incoming | tr.PI |
| 232 | TR_POUT | Trend mean-value P outgoing | tr.PD |
| 233 | TR_QIND | Trend mean-value Q inductive | tr.9L |
| 234 | TR_QCAP | Trend mean-value Q capacitive | tr. 95 |
| 235 | TR_QIN | Trend mean-value Q incoming | tr. 91 |
| 236 | TR_QOUT | Trend mean-value Q outgoing | tr. 90 |
| 237 | TR_S | Trend mean-value S | tr. 5 |
| 238 | M_PIN_MIN | Maximum mean-value P incoming | P.ınc |
| 239 | M_POUT_MIN | Maximum mean-value P outgoing | P.oUt |
| 240 | M_QIND_MIN | Maximum mean-value Q inductive | 9. ind |
| 241 | M_QCAP_MIN | Maximum mean-value Q capacitive | 9.c RP |
| 242 | M_QIN_MIN | Maximum mean-value Q incoming | Q. $1 \cap$ |
| 243 | M_QOUT_MIN | Maximum mean-value Q outgoing | 9.alt |
| 244 | M_S_MIN | Maximum mean-value $S$ | 5 |
| 245 | M_PIN_MAX | Minimum mean-value $P$ incoming | P.inc |
| 246 | M_POUT_MAX | Minimum mean-value $P$ outgoing | P.alt |
| 247 | M_QIND_MAX | Minimum mean-value Q inductive | Q. ind |
| 248 | M_QCAP_MAX | Minimum mean-value $Q$ capacitive | 9.cRP |
| 249 | M_QIN_MAX | Minimum mean-value Q incoming | 9. 1 ¢ |
| 250 | M_QOUT_MAX | Minimum mean-value Q outgoing | 9.aリt |
| 251 | M_S_MAX | Minimum mean-value S | 5 |
| 252 | M1 | Mean-value 1 | 71 |
| 253 | M2 | Mean-value 2 | $\overline{11}$ |
| 254 | M3 | Mean-value 3 | $\overline{7}$ |
| 255 | M4 | Mean-value 4 | $\overline{11}$ |
| 256 | M5 | Mean-value 5 | $\overline{115}$ |
| 257 | M6 | Mean-value 6 | $\overline{115}$ |
| 258 | M7 | Mean-value 7 | $\therefore 7$ |
| 259 | M8 | Mean-value 8 | $\overline{11}$ |
| 260 | M9 | Mean-value 9 | $\overline{11} 9$ |
| 261 | M10 | Mean-value 10 | י10 |
| 262 | M11 | Mean-value 11 | -11 |


| No． | Name | Description | Name（Display） |
| :---: | :---: | :---: | :---: |
| 263 | M12 | Mean－value 12 | － 12 |
| 264 | TR＿1 | Trend mean－value 1 | tr 1 |
| 265 | TR＿2 | Trend mean－value 2 | tr 2 |
| 266 | TR＿3 | Trend mean－value 3 | $\operatorname{tr} 3$ |
| 267 | TR＿4 | Trend mean－value 4 | tr 4 |
| 268 | TR＿5 | Trend mean－value 5 | tr 5 |
| 269 | TR＿6 | Trend mean－value 6 | tr 5 |
| 270 | TR＿7 | Trend mean－value 7 | tr 7 |
| 271 | TR＿8 | Trend mean－value 8 | $\operatorname{tr} B$ |
| 272 | TR＿9 | Trend mean－value 9 | tr 9 |
| 273 | TR＿10 | Trend mean－value 10 | Lr IV |
| 274 | TR＿11 | Trend mean－value 11 | tr 11 |
| 275 | TR＿12 | Trend mean－value 12 | Er t2 |
| 276 | M1＿MIN | Maximum mean－value 1 | $\overline{11}$ |
| 277 | M2＿MIN | Maximum mean－value 2 | $\overline{11}$ |
| 278 | M3＿MIN | Maximum mean－value 3 | $\overline{7}$ |
| 279 | M4＿MIN | Maximum mean－value 4 | $\overline{11}$ |
| 280 | M5＿MIN | Maximum mean－value 5 | $\overline{11}$ |
| 281 | M6＿MIN | Maximum mean－value 6 | י 6 |
| 282 | M7＿MIN | Maximum mean－value 7 | $\overline{7} 7$ |
| 283 | M8＿MIN | Maximum mean－value 8 | $\overline{11}$ |
| 284 | M9＿MIN | Maximum mean－value 9 | $\overline{7} 9$ |
| 285 | M10＿MIN | Maximum mean－value 10 | $\therefore 10$ |
| 286 | M11＿MIN | Maximum mean－value 11 | 二11 |
| 287 | M12＿MIN | Maximum mean－value 12 | －12 |
| 288 | M1＿MAX | Minimum mean－value 1 | $\overline{11}$ |
| 289 | M2＿MAX | Minimum mean－value 2 | $\bar{\square}$ |
| 290 | M3＿MAX | Minimum mean－value 3 | $\overline{7}$ |
| 291 | M4＿MAX | Minimum mean－value 4 | $\overline{11}$ |
| 292 | M5＿MAX | Minimum mean－value 5 | $\overline{7}$ |
| 293 | M6＿MAX | Minimum mean－value 6 | $\overline{11}$ |
| 294 | M7＿MAX | Minimum mean－value 7 | $\overline{7}$ |
| 295 | M8＿MAX | Minimum mean－value 8 | $\overline{11}$ |
| 296 | M9＿MAX | Minimum mean－value 9 | $\overline{11}$ |
| 297 | M10＿MAX | Minimum mean－value 10 | $\square 10$ |
| 298 | M11＿MAX | Minimum mean－value 11 | －11 |
| 299 | M12＿MAX | Minimum mean－value 12 | י12 |
| 300 | AOUT1 | Analog output 1 | R 1 |
| 301 | AOUT2 | Analog output 2 | Rロ己 |
| 302 | AOUT3 | Analog output 3 | Rロヨ |
| 303 | AOUT4 | Analog output 4 | RD4 |
| 304 | PIN＿HT | Meter P incoming high tariff | Pl．Ht |
| 305 | POUT＿HT | Meter P outgoing high tariff | PD．Ht |
| 306 | QIND＿HT | Meter Q inductive high tariff | 9L．HL |
| 307 | QCAP＿HT | Meter Q capacitive high tariff | 9L．HL |
| 308 | QIN＿HT | Meter Q incoming high tariff | 91．HL |
| 309 | QOUT＿HT | Meter Q outgoing high tariff | 90．HL |
| 310 | PIN＿LT | Meter P incoming low tariff | Pl．Lt |
| 311 | POUT＿LT | Meter P outgoing low tariff | PD．LE |
| 312 | QIND＿LT | Meter Q inductive low tariff | 9L．Lt |
| 313 | QCAP＿LT | Meter Q capacitive low tariff | 9C．LE |
| 314 | QIN＿LT | Meter Q incoming low tariff | $91 . L t$ |
| 315 | QOUT＿LT | Meter Q outgoing low tariff | 90．LE |
| 316 | P1IN＿HT | Meter P1 incoming high tariff | P II．H |


| No． | Name | Description | Name（Display） |
| :---: | :---: | :---: | :---: |
| 317 | P2IN＿HT | Meter P2 incoming high tariff | P리．H |
| 318 | P3IN＿HT | Meter P3 incoming high tariff | Pヨ1．H |
| 319 | Q1IN＿HT | Meter Q1 incoming high tariff | 9 II．H |
| 320 | Q2IN＿HT | Meter Q2 incoming high tariff | 9 21.4 |
| 321 | Q3IN＿HT | Meter Q3 incoming high tariff | $931 . H$ |
| 322 | P1IN＿LT | Meter P1 incoming low tariff | P II．L |
| 323 | P2IN＿LT | Meter P2 incoming low tariff | Pこl．L |
| 324 | P3IN＿LT | Meter P3 incoming low tariff | P31．L |
| 325 | Q1IN＿LT | Meter Q1 incoming low tariff | $91 / . L$ |
| 326 | Q2IN＿LT | Meter Q2 incoming low tariff | 9ㄹI．L |
| 327 | Q3IN＿LT | Meter Q3 incoming low tariff | $931 . L$ |
| 328 | CNTR＿IO2＿HT | Meter I／O 2 high tariff | E $2 . H$ |
| 329 | CNTR＿IO6＿HT | Meter I／O 6 high tariff | E E．H |
| 330 | CNTR＿IO7＿HT | Meter I／O 7 high tariff | E 7 H |
| 331 | CNTR＿IO8＿HT | Meter I／O 8 high tariff | E B．H |
| 332 | CNTR＿IO9＿HT | Meter I／O 9 high tariff | E $9 . H$ |
| 333 | CNTR＿IO10＿HT | Meter I／O 10 high tariff | EID．H |
| 334 | CNTR＿IO11＿HT | Meter I／O 11 high tariff | E｜ 1 H |
| 335 | CNTR＿IO2＿LT | Meter I／O 2 low tariff | E $2 . L$ |
| 336 | CNTR＿IO6＿LT | Meter I／O 6 low tariff | E E．L |
| 337 | CNTR＿IO7＿LT | Meter I／O 7 low tariff | $E 7 \mathrm{~L}$ |
| 352 | CNTR＿IO8＿LT | Meter I／O 8 low tariff | E B．L |
| 353 | CNTR＿IO9＿LT | Meter I／O 9 low tariff | E 9．L |
| 354 | CNTR＿IO10＿LT | Meter I／O 10 low tariff | EID．L |
| 355 | CNTR＿IO11＿LT | Meter I／O 11 low tariff | E I IL |
| 356 | RTC＿UTC | UTC time in seconds since January 1st 1970 | ULE．L |
| 357 | EV＿TIME | UTC time of last event | Eut．t |
| 358 | OPR＿CNTR | Operating hour counter APLUS | DL［ |
| 359 | OPR＿CNTR1 | Resettable operating hour counter 1 | 䛧 1 |
| 360 | OPR＿CNTR2 | Resettable operating hour counter 2 | －ロ［2 |
| 361 | OPR＿CNTR3 | Resettable operating hour counter 3 | － 1 ［3 |
| 362 | RTC＿LOCAL | Local time in seconds since January 1st 1970 | L［L．L |
| $\begin{aligned} & 363 \\ & 424 \end{aligned}$ | $\begin{aligned} & \text { H2_U1X } \\ & : \\ & \text { H63_U1X } \end{aligned}$ | Voltage phase 1：content of 2nd harmonic ： <br> Voltage phase 1：content of 63rd harmonic |  |
| $\begin{aligned} & 425 \\ & 486 \end{aligned}$ | $\begin{aligned} & \text { H2_U2X } \\ & : \\ & \text { H63_U2X } \end{aligned}$ | Voltage phase 2：content of 2nd harmonic ： <br> Voltage phase 2：content of 63rd harmonic |  |
| $\begin{gathered} 487 \\ 548 \end{gathered}$ | $\begin{aligned} & \text { H2_U3X } \\ & : \\ & \text { H63_U3X } \\ & \hline \end{aligned}$ | Voltage phase 3：content of 2nd harmonic ： <br> Voltage phase 3：content of 63rd harmonic |  |
| $\begin{array}{r} 549 \\ 610 \\ \hline \end{array}$ | $\begin{aligned} & \text { H2_I1X } \\ & : \\ & \text { H63_I1X } \\ & \hline \end{aligned}$ | Current phase 1：content of 2nd harmonic Current phase 1：content of 63rd harmonic |  |
| $\begin{aligned} & 611 \\ & 672 \end{aligned}$ | $\begin{aligned} & \text { H2_I2X } \\ & : \\ & \text { H63_I2X } \\ & \hline \end{aligned}$ | Current phase 2：content of 2nd harmonic Current phase 2：content of 63rd harmonic |  |
| 673 734 | $\begin{aligned} & \text { H2_I3X } \\ & : \\ & \text { H63_I3X } \end{aligned}$ | Current phase 3：content of 2nd harmonic Current phase 3：content of 63rd harmonic |  |
| 735 | $\begin{aligned} & \text { H2_U1X_MAX } \\ & : \\ & \text { H63_U1X_MAX } \end{aligned}$ | Voltage phase 1：max．content of 2nd harmonic ： <br> Voltage phase 1：max．content of 63rd harmonic |  |
| 797 858 | $\begin{aligned} & \text { H2_U2X_MAX } \\ & : \\ & \text { H63_U2X_MAX } \end{aligned}$ | Voltage phase 2：max．content of 2nd harmonic Voltage phase 2：max．content of 63rd harmonic |  |


| No. | Name | Description | Name (Display) |
| :--- | :--- | :--- | :--- |
| 859 | H2_U3X_MAX <br> 920 | H63_U3X_MAX | Voltage phase 3: max. content of 2nd harmonic |
| 921 | H2_I1X_MAX <br> $:$ | Voltage phase 3: max. content of 63rd harmonic |  |
| 982 | H63_I1X_MAX | Current phase 1: max. content of 2nd harmonic <br> H2_I2X_MAX <br> 1044 | H63_I2X_MAX |

B1 Display matrix single phase system

| U_MAX U U_MIN |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| I_MAX | $\begin{aligned} & \text { IB } \\ & \text { IB_MAX } \end{aligned}$ |  |  |  |  |  |
| P_MAX |  |  |  |  |  |  |
| Q |  |  |  |  |  |  |
| S |  |  |  |  |  |  |
| PF <br> PF_MIN_IN_L <br> PF_MIN_IN_C | PF <br> PF_MIN_OUT_L <br> PF_MIN_OUT_C | $\begin{array}{\|l\|} \hline \text { PFG } \\ \text { PFG_MIN_IN_L } \\ \text { PFG_MIN_IN_C } \end{array}$ | $\begin{array}{\|l\|} \hline \text { PFG } \\ \text { PFG_MIN_OUT_L } \\ \text { PFG_MIN_OUT_C } \\ \hline \end{array}$ |  |  |  |
| $\begin{array}{\|l} \text { F_MAX } \\ \text { F } \\ \text { F_MIN } \\ \hline \end{array}$ |  |  |  |  |  |  |
| $\left\lvert\, \begin{aligned} & P \\ & Q \\ & Q \\ & S \end{aligned}\right.$ | P | $\begin{aligned} & \mathrm{P} \\ & \mathrm{Q} \\ & \mathrm{PF} \end{aligned}$ | $\begin{aligned} & \mathrm{P} \\ & \mathrm{~S} \\ & \mathrm{~F} \\ & \hline \end{aligned}$ | $\begin{aligned} & \hline \mathrm{P} \\ & \mathrm{QG} \\ & \mathrm{TG} \end{aligned}$ |  |  |
| D | QG |  |  |  |  |  |
| dd.mm <br> hh.mm ss | $\begin{aligned} & \text { OPR_CNTR1 } \\ & \text { OPR_CNTR2 } \\ & \text { OPR_CNTR3 } \end{aligned}$ | OPR_CNTR |  |  |  |  |
| $\begin{aligned} & \text { THD_U } \\ & \text { THD_U_MAX } \end{aligned}$ |  |  |  |  |  |  |
| $\begin{aligned} & \hline \text { TDD_I } \\ & \text { TDD_I_MAX } \end{aligned}$ |  |  |  |  |  |  |
| Block with mean values of power quantities |  |  |  |  |  |  |
| $\begin{aligned} & \mathrm{H} 2 \_\mathrm{U} \\ & \mathrm{H} 2 \_\mathrm{U} \text { MAX } \end{aligned}$ | $\begin{aligned} & \mathrm{H} 3 \_U \\ & \text { H3_U_MAX } \end{aligned}$ | $\begin{aligned} & \mathrm{H} 4 \_\mathrm{U} \\ & \mathrm{H} 4 \_\mathrm{U} \text { _MAX } \end{aligned}$ | ... | $\begin{array}{\|l\|} \hline \text { H48_U } \\ \text { H48_U_MAX } \end{array}$ | $\begin{aligned} & \mathrm{H} 49 \text { _U } \\ & \text { H49_U_MAX } \end{aligned}$ | $\begin{aligned} & \text { H50_U } \\ & \text { H50_U_MAX } \end{aligned}$ |
| $\begin{aligned} & \hline \text { H2_I } \\ & \text { H2_I_MAX } \end{aligned}$ | $\begin{aligned} & \text { H3_I } \\ & \text { H3_I_MAX } \end{aligned}$ | $\begin{aligned} & \text { H4_I } \\ & \text { H4_I_MAX } \end{aligned}$ | $\ldots$ | $\begin{array}{\|l\|} \hline \text { H48_I } \\ \text { H48_I_MAX } \end{array}$ | $\begin{aligned} & \text { H49_I } \\ & \text { H49_I_MAX } \end{aligned}$ | $\begin{aligned} & \text { H50_I } \\ & \text { H50_I_MAX } \end{aligned}$ |

## B2 Display matrix Split-phase (two-phase) systems

| U1N | U1N_MAX | U1N_MIN | UNE |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| U2N | U2N_MAX | U2N_MIN | UNE_MAX |  |  |  |
| U | U_MAX | U_MIN |  |  |  |  |
| 11 | I1_MAX | IB1 | IB1_MAX |  |  |  |
| 12 | 12_MAX | IB2 | IB2_MAX |  |  |  |
| P1 | P1_MAX |  |  |  |  |  |
| P2 | P2_MAX |  |  |  |  |  |
| P | P_MAX |  |  |  |  |  |
| Q1 | Q1_MAX |  |  |  |  |  |
| Q2 | Q2_MAX |  |  |  |  |  |
| Q | Q_MAX |  |  |  |  |  |
| S1 | S1_MAX |  |  |  |  |  |
| S2 | S2_MAX |  |  |  |  |  |
| S | S_MAX |  |  |  |  |  |
| PF | PF | PF | PFG | PFG | PFG |  |
| PF1 | PF_MIN_IN_L | PF_MIN_OUT_L | PFG1 | PFG_MIN_IN_L | PFG_MIN_OUT_L |  |
| PF2 | PF_MIN_IN_C | PF_MIN_OUT_C | PFG2 | PFG_MIN_IN_C | PFG_MIN_OUT_C |  |
| F_MAX |  |  |  |  |  |  |
| F |  |  |  |  |  |  |
| F_MIN |  |  |  |  |  |  |
| P | P | P | P | P |  |  |
| Q | U_MEAN | Q | S | QG |  |  |
| S | I_MEAN | PF | F | TG |  |  |
| P1 | P2 | U1N | U2N |  |  |  |
| Q1 | Q2 | 11 | 12 |  |  |  |
| S1 | S2 | P1 | P2 |  |  |  |
| D1 | D1_MAX |  | QG1 | QG1_MAX | QG |  |
| D2 | D2_MAX | D_MAX | QG2 | QG2_MAX | QG_MAX |  |
| dd.mm | OPR_CNTR1 | OPR_CNTR |  |  |  |  |
| hh.mm | OPR_CNTR2 |  |  |  |  |  |
| ss | OPR_CNTR3 |  |  |  |  |  |
| THD_U1N | THD_U2N |  |  |  |  |  |
| THD_U1N_MAX | THD_U2N_MAX |  |  |  |  |  |
| TDD_I1 | TDD_12 |  |  |  |  |  |
| TDD_I1_MAX | TDD_I2_MAX |  |  |  |  |  |
| Block with mean values of power quantities |  |  |  |  |  |  |
| H2_U1N | H3_U1N | 4_U1N | ... | H48_U1N | H49_U1N | H50_U1N |
| H2_U1N_MAX | H3_U1N_MAX | H4_U1N_MAX |  | H48_U1N_MAX | H49_U1N_MAX | H50_U1N_MAX |
| H2_U2N | H3_U2N | H4_U2N | ... | H48_U2N | H49_U2N | H50_U2N |
| H2_U2N_MAX | H3_U2N_MAX | H4_U2N_MAX |  | H48_U2N_MAX | H49_U2N_MAX | H50_U2N_MAX |
| H2_I1 | H3_11 | H4_11 | ... | H48_I1 | H49_I1 | H50_I1 |
| H2_I1_MAX | H3_I1_MAX | H4_I1_MAX |  | H48_I1_MAX | H49_I1_MAX | H50_I1_MAX |
| H2_I2 | H3_12 | H4_12 | ... | H48_12 | H49_12 | H50_I2 |
| H2_I2_MAX | H3_I2_MAX | H4_I2_MAX |  | H48_12_MAX | H49_I2_MAX | H50_I2_MAX |

B3 Display matrix 3-wire system, balanced load


| Block with mean-values of power quantities |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\begin{aligned} & \text { H2_U12 } \\ & \text { H2_U12_MAX } \end{aligned}$ | $\begin{aligned} & \text { H3_U12 } \\ & \text { H3_U12_MAX } \end{aligned}$ | $\begin{aligned} & \text { H4_U12 } \\ & \text { H4_U12_MAX } \end{aligned}$ | ... | $\begin{array}{\|l} \hline \text { H48_U12 } \\ \text { H48_U12_MAX } \end{array}$ | $\begin{aligned} & \text { H49_U12 } \\ & \text { H49_U12_MAX } \end{aligned}$ | $\begin{aligned} & \text { H50_U12 } \\ & \text { H50_U12_MAX } \end{aligned}$ |
| $\begin{aligned} & \text { H2_U23 } \\ & \text { H2_U23_MAX } \end{aligned}$ | $\begin{aligned} & \text { H3_U23 } \\ & \text { H3_U23_MAX } \end{aligned}$ | $\begin{aligned} & \mathrm{H} 4 \_U 23 \\ & \text { H4_U23_MAX } \end{aligned}$ | ... | $\begin{array}{\|l\|} \hline \text { H48_U23 } \\ \text { H48_U23_MAX } \end{array}$ | $\begin{aligned} & \text { H49_U23 } \\ & \text { H49_U23_MAX } \end{aligned}$ | $\begin{aligned} & \text { H50_U23 } \\ & \text { H50_U23_MAX } \end{aligned}$ |
| $\begin{aligned} & \text { H2_U31 } \\ & \text { H2_U31_MAX } \end{aligned}$ | $\begin{aligned} & \text { H3_U31 } \\ & \text { H3_U31_MAX } \end{aligned}$ | $\begin{aligned} & \text { H4_U31 } \\ & \text { H4_U31_MAX } \end{aligned}$ | ... | $\begin{array}{\|l\|} \hline \text { H48_U31 } \\ \text { H48_U31_MAX } \end{array}$ | $\begin{aligned} & \text { H49_U31 } \\ & \text { H49_U31_MAX } \end{aligned}$ | $\begin{aligned} & \text { H50_U31 } \\ & \text { H50_U31_MAX } \end{aligned}$ |
| $\begin{aligned} & \text { H2_I } \\ & \text { H2_I_MAX } \end{aligned}$ | $\begin{array}{\|l\|} \hline \text { H3_I } \\ \text { H3_I_MAX } \end{array}$ | $\begin{aligned} & \hline \text { H4_I } \\ & \text { H4_I_MAX } \end{aligned}$ | $\ldots$ | $\begin{aligned} & \text { H48_I } \\ & \text { H48_I_MAX } \end{aligned}$ | $\begin{aligned} & \text { H49_I } \\ & \text { H49_I_MAX } \end{aligned}$ | $\begin{aligned} & \text { H50_I } \\ & \text { H50_I_MAX } \end{aligned}$ |

B4 Display matrix 3-wire systems, unbalanced load

| $\begin{aligned} & \text { U12 } \\ & \text { U23 } \\ & \text { U31 } \end{aligned}$ | $\begin{aligned} & \text { U12_MAX } \\ & \text { U23_MAX } \\ & \text { U31_MAX } \end{aligned}$ | $\begin{aligned} & \text { U12_MIN } \\ & \text { U23_MIN } \\ & \text { U31_MIN } \end{aligned}$ | DEV_UMAX DEV_UMAX_MAX |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\begin{aligned} & \hline \text { UR1 } \\ & \text { UR2 } \\ & \text { UO } \\ & \hline \end{aligned}$ | $\begin{array}{\|l\|} \hline \text { UNB_UR2_UR1 } \\ \text { UNB_UR2_UR1_MAX } \end{array}$ |  |  |  |  |  |
| $\begin{array}{\|l} 11 \\ 12 \\ 13 \\ \hline \end{array}$ | $\begin{aligned} & \text { I1_MAX } \\ & \text { I2_MAX } \\ & \text { I3_MAX } \end{aligned}$ | $\begin{array}{\|l\|} \hline \text { IB1 } \\ \text { IB2 } \\ \text { IB3 } \end{array}$ | $\begin{aligned} & \text { IB1_MAX } \\ & \text { IB2_MAX } \\ & \text { IB3_MAX } \end{aligned}$ | $\begin{aligned} & \text { DEV_IMAX } \\ & \text { DEV_IMAX_MAX } \end{aligned}$ |  |  |
| $\begin{array}{\|l\|l} \hline \text { R1 } 1 \\ \text { IR2 } \\ \text { I0 } \\ \hline \end{array}$ | $\begin{aligned} & \text { UNB_IR2_IR1 } \\ & \text { UNB_IR2_IR1_MAX } \end{aligned}$ |  |  |  |  |  |
| P_MAX |  |  |  |  |  |  |
| Q |  |  |  |  |  |  |
| $\begin{aligned} & \text { S } \\ & \text { S_MAX } \end{aligned}$ |  |  |  |  |  |  |
| $\begin{array}{\|l\|} \hline \text { PF } \\ \text { PF_MIN_IN_L } \\ \text { PF_MIN_IN_C } \\ \hline \end{array}$ | $\begin{array}{\|l\|} \hline \text { PF } \\ \text { PF_MIN_OUT_L } \\ \text { PF_MIN_OUT_C } \\ \hline \end{array}$ | $\begin{array}{\|l} \hline \text { PFG } \\ \text { PFG_MIN_IN_L } \\ \text { PFG_MIN_IN_C } \end{array}$ | $\begin{array}{\|l} \hline \text { PFG } \\ \text { PFG_MIN_OUT_L } \\ \text { PFG_MIN_OUT_C } \end{array}$ |  |  |  |
| $\begin{aligned} & \text { F_MAX } \\ & \text { F } \\ & \text { F_MIN } \end{aligned}$ |  |  |  |  |  |  |
| $\begin{array}{\|l} \hline \mathrm{P} \\ \mathrm{Q} \\ \mathrm{~S} \\ \hline \end{array}$ | $\begin{aligned} & \text { P } \\ & \text { U_MEAN } \\ & \text { I_MEAN } \end{aligned}$ | $\begin{array}{\|l\|} \hline \mathrm{P} \\ \mathrm{Q} \\ \mathrm{PF} \\ \hline \end{array}$ | $\begin{aligned} & P \\ & \mathrm{~S} \\ & \mathrm{~F} \\ & \hline \end{aligned}$ | $\begin{aligned} & \hline \mathrm{P} \\ & \mathrm{QG} \\ & \mathrm{TG} \end{aligned}$ |  |  |
| D | $\begin{aligned} & \text { QG } \\ & \text { QG_MAX } \end{aligned}$ |  |  |  |  |  |
| dd.mm <br> hh.mm ss | OPR_CNTR1 OPR_CNTR2 OPR_CNTR3 | OPR_CNTR |  |  |  |  |
| $\begin{aligned} & \text { THD_U12 } \\ & \text { THD_U12_MAX } \end{aligned}$ | $\begin{aligned} & \hline \text { THD_U23 } \\ & \text { THD_U23_MAX } \end{aligned}$ | $\begin{aligned} & \hline \text { THD_U31 } \\ & \text { THD_U31_MAX } \end{aligned}$ |  |  |  |  |
| $\begin{aligned} & \hline \text { TDD_I1 } \\ & \text { TDD_I__MAX } \end{aligned}$ | $\begin{aligned} & \text { TDD_I2 } \\ & \text { TDD_I2_MAX } \end{aligned}$ | $\begin{aligned} & \text { TDD_I3 } \\ & \text { TDD_I3_MAX } \end{aligned}$ |  |  |  |  |
| Block with mean-values of power quantities |  |  |  |  |  |  |
| $\begin{aligned} & \mathrm{H} 2 \_\mathrm{U} 12 \\ & \mathrm{H} 2 \_\mathrm{U} 12 \_\mathrm{MAX} \end{aligned}$ | $\begin{aligned} & \text { H3_U12 } \\ & \text { H3_U12_MAX } \end{aligned}$ | $\begin{aligned} & \text { H4_U12 } \\ & \text { H4_U12_MAX } \end{aligned}$ | ... | $\begin{aligned} & \text { H48_U12 } \\ & \text { H48_U12_MAX } \end{aligned}$ | $\begin{aligned} & \text { H49_U12 } \\ & \text { H49_U12_MAX } \end{aligned}$ | $\begin{aligned} & \text { H50_U12 } \\ & \text { H50_U12_MAX } \end{aligned}$ |
| $\begin{aligned} & \mathrm{H} 2 \_U 23 \\ & \text { H2_U23_MAX } \end{aligned}$ | $\begin{aligned} & \text { H3_U23 } \\ & \text { H3_U23_MAX } \end{aligned}$ | $\begin{aligned} & \text { H4_U23 } \\ & \text { H4_U23_MAX } \end{aligned}$ | ... | $\begin{aligned} & \text { H48_U23 } \\ & \text { H48_U23_MAX } \end{aligned}$ | $\begin{aligned} & \text { H49_U23 } \\ & \text { H49_U23_MAX } \end{aligned}$ | $\begin{aligned} & \hline \text { H50_U23 } \\ & \text { H50_U23_MAX } \end{aligned}$ |
| $\begin{aligned} & \mathrm{H} 2 \_U 31 \\ & \mathrm{H} 2 \_U 31 \_M A X \end{aligned}$ | $\begin{aligned} & \text { H3_U31 } \\ & \text { H3_U31_MAX } \end{aligned}$ | $\begin{aligned} & \text { H4_U31 } \\ & \text { H4_U31_MAX } \end{aligned}$ | ... | $\begin{array}{\|l} \hline \text { H48_U31 } \\ \text { H48_U31_MAX } \end{array}$ | $\begin{aligned} & \text { H49_U31 } \\ & \text { H49_U31_MAX } \end{aligned}$ | $\begin{aligned} & \text { H50_U31 } \\ & \text { H50_U31_MAX } \end{aligned}$ |
| $\begin{aligned} & \hline \text { H2_I1 } \\ & \text { H2_I1_MAX } \end{aligned}$ | $\begin{aligned} & \mathrm{H3} \text { _I } 11 \\ & \mathrm{H3} \text { _I_ } 11 . \mathrm{MAX} \end{aligned}$ | $\begin{aligned} & \text { H4_I1 } \\ & \text { H4_I1_MAX } \end{aligned}$ | ... | $\begin{array}{\|l\|} \hline \text { H48_I1 } \\ \text { H48_I__MAX } \end{array}$ | $\begin{aligned} & \hline \text { H49_I1 } \\ & \text { H49_I1_MAX } \end{aligned}$ | $\begin{aligned} & \text { H50_I1 } \\ & \text { H50_I__MAX } \end{aligned}$ |
| $\begin{aligned} & \mathrm{H} 2 \_12 \\ & \mathrm{H} 2 \_12 \_\mathrm{MAX} \end{aligned}$ | $\begin{aligned} & \mathrm{H3} \text { _I2 } \\ & \text { H3_I2_MAX } \end{aligned}$ | $\begin{aligned} & \text { H4_I2 } \\ & \text { H4_I2_MAX } \end{aligned}$ | ... | $\begin{aligned} & \text { H48_I2 } \\ & \text { H48_I2_MAX } \end{aligned}$ | $\begin{array}{\|l\|} \hline \text { H49_I2 } \\ \text { H49_12_MAX } \end{array}$ | $\begin{aligned} & \text { H50_I2 } \\ & \text { H50_I2_MAX } \end{aligned}$ |
| $\begin{aligned} & \mathrm{H} 2 \_13 \\ & \mathrm{H} 2 \_13 \_\mathrm{MAX} \end{aligned}$ | $\begin{aligned} & \text { H3_I3 } \\ & \text { H3_I3_MAX } \end{aligned}$ | $\begin{aligned} & \text { H4_I3 } \\ & \text { H4_I3_MAX } \end{aligned}$ | ... | $\begin{array}{\|l} \mathrm{H} 48 \text { _I3 } \\ \text { H48_13_MAX } \end{array}$ | $\begin{aligned} & \text { H49_I3 } \\ & \text { H49_13_MAX } \end{aligned}$ | $\begin{aligned} & \text { H50_I3 } \\ & \text { H50_I3_MAX } \end{aligned}$ |

B5 Display matrix 3-wire systems, unbalanced load, Aron

| $\begin{aligned} & \text { U12 } \\ & \text { U23 } \\ & \text { U31 } \end{aligned}$ | $\begin{aligned} & \text { U12_MAX } \\ & \text { U23_MAX } \\ & \text { U31_MAX } \end{aligned}$ | $\begin{aligned} & \text { U12_MIN } \\ & \text { U23_MIN } \\ & \text { U31_MIN } \end{aligned}$ | $\begin{aligned} & \text { DEV_UMAX } \\ & \text { DEV_UMAX_MAX } \end{aligned}$ |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\begin{aligned} & \text { UR1 } \\ & \text { UR2 } \\ & \text { U0 } \end{aligned}$ | UNB_UR2_UR1 <br> UNB_UR2_UR1_MAX |  |  |  |  |  |
| $\begin{aligned} & 11 \\ & 12 \\ & 13 \end{aligned}$ | $\begin{aligned} & \text { I1_MAX } \\ & \text { I2_MAX } \\ & \text { I3_MAX } \\ & \hline \end{aligned}$ | $\begin{array}{\|l\|l\|} \hline \mathrm{IB} 1 \\ \mathrm{IB2} \\ \mathrm{IB} 3 \\ \hline \end{array}$ | $\begin{aligned} & \text { IB1_MAX } \\ & \text { IB2_MAX } \\ & \text { IB3_MAX } \end{aligned}$ | $\begin{aligned} & \text { DEV_IMAX } \\ & \text { DEV_IMAX_MAX } \end{aligned}$ |  |  |
| $P$ |  |  |  |  |  |  |
| $\begin{aligned} & \mathrm{Q} \\ & \mathrm{Q} \text { _MAX } \end{aligned}$ |  |  |  |  |  |  |
| $\begin{aligned} & \text { S } \\ & \text { S_MAX } \end{aligned}$ |  |  |  |  |  |  |
| $\begin{aligned} & \text { PF } \\ & \text { PF_MIN_IN_L } \\ & \text { PF_MIN_IN_C } \end{aligned}$ | $\begin{array}{\|l} \hline \text { PF } \\ \text { PF_MIN_OUT_L } \\ \text { PF_MIN_OUT_C } \end{array}$ | $\begin{aligned} & \text { PFG } \\ & \text { PFG_MIN_IN_L } \\ & \text { PFG_MIN_IN_C } \end{aligned}$ | $\begin{aligned} & \text { PFG } \\ & \text { PFG_MIN_OUT_L } \\ & \text { PFG_MIN_OUT_C } \end{aligned}$ |  |  |  |
| $\begin{aligned} & \text { F_MAX } \\ & \text { F } \\ & \text { F_MIN } \end{aligned}$ |  |  |  |  |  |  |
| $\left\lvert\, \begin{aligned} & \mathrm{P} \\ & \mathrm{Q} \\ & \mathrm{~S} \end{aligned}\right.$ | ```P U_MEAN MEAN``` | $\begin{aligned} & \mathrm{P} \\ & \mathrm{Q} \\ & \mathrm{PF} \end{aligned}$ | $\left\lvert\, \begin{aligned} & P \\ & S \\ & F \end{aligned}\right.$ | $\begin{aligned} & \mathrm{P} \\ & \mathrm{QGG} \\ & \mathrm{TG} \\ & \hline \end{aligned}$ |  |  |
| $\begin{aligned} & \hline D \\ & D \_M A X \end{aligned}$ | QG |  |  |  |  |  |
| dd.mm <br> hh.mm <br> SS | $\begin{array}{\|l\|} \hline \text { OPR_CNTR1 } \\ \text { OPR_CNTR2 } \\ \text { OPR_CNTR3 } \\ \hline \end{array}$ | OPR_CNTR |  |  |  |  |
| $\begin{aligned} & \hline \text { THD_U12 } \\ & \text { THD_U12_MAX } \end{aligned}$ | $\begin{aligned} & \text { THD_U23 } \\ & \text { THD_U23_MAX } \end{aligned}$ | $\begin{aligned} & \text { THD_U31 } \\ & \text { THD_U31_MAX } \end{aligned}$ |  |  |  |  |
| $\begin{aligned} & \hline \text { TDD_I1 } \\ & \text { TDD_I1_MAX } \end{aligned}$ | $\begin{aligned} & \text { TDD_I2 } \\ & \text { TDD_12_MAX } \end{aligned}$ | $\begin{aligned} & \text { TDD_I3 } \\ & \text { TDD_I3_MAX } \end{aligned}$ |  |  |  |  |
| Block with mean-values of power quantities |  |  |  |  |  |  |
| $\begin{aligned} & \hline \text { H2_U12 } \\ & \text { H2_U12_MAX } \end{aligned}$ | $\begin{aligned} & \text { H3_U12 } \\ & \text { H3_U12_MAX } \end{aligned}$ | $\begin{aligned} & \text { H4_U12 } \\ & \text { H4_U12_MAX } \end{aligned}$ | ... | $\begin{aligned} & \text { H48_U12 } \\ & \text { H48_U12_MAX } \end{aligned}$ | $\begin{aligned} & \mathrm{H} 49 \_U 12 \\ & \mathrm{H} 49 \_\mathrm{U} 12 \_\mathrm{MAX} \end{aligned}$ | $\begin{aligned} & \text { H50_U12 } \\ & \text { H50_U12_MAX } \end{aligned}$ |
| $\begin{aligned} & \mathrm{H} 2 \_U 23 \\ & \mathrm{H} 2 \_U 23 \_M A X \end{aligned}$ | $\begin{aligned} & \text { H3_U23 } \\ & \text { H3_U23_MAX } \end{aligned}$ | $\begin{aligned} & \text { H4_U23 } \\ & \text { H4_U23_MAX } \end{aligned}$ | $\cdots$ | $\begin{aligned} & \text { H48_U23 } \\ & \text { H48_U23_MAX } \end{aligned}$ | $\left\lvert\, \begin{aligned} & \mathrm{H} 49 \_U 23 \\ & \mathrm{H} 49 \_U 23 \_M A X \end{aligned}\right.$ | $\begin{aligned} & \mathrm{H} 50 \_ \text {U23 } \\ & \mathrm{H} 50 \_ \text {U23_MAX } \end{aligned}$ |
| $\begin{aligned} & \mathrm{H} 2 \_ \text {U31 } \\ & \text { H2_U31_MAX } \end{aligned}$ | $\begin{aligned} & \text { H3_U31 } \\ & \text { H3_U31_MAX } \end{aligned}$ | $\begin{aligned} & \mathrm{H} 4 \_ \text {U31 } \\ & \text { H4_U31_MAX } \end{aligned}$ | ... | $\begin{array}{\|l} \text { H48_U31 } \\ \text { H48_U31_MAX } \end{array}$ | $\begin{aligned} & \mathrm{H} 49 \_ \text {U31 } \\ & \text { H49_U31_MAX } \end{aligned}$ | $\begin{aligned} & \text { H50_U31 } \\ & \text { H50_U31_MAX } \end{aligned}$ |
| $\begin{aligned} & \hline \text { H2_I1 } \\ & \text { H2_I1_MAX } \end{aligned}$ | $\begin{aligned} & \text { H3_I1 } \\ & \text { H3_I1_MAX } \end{aligned}$ | $\begin{aligned} & \text { H4_I1 } \\ & \text { H4_I1_MAX } \end{aligned}$ | ... | $\begin{aligned} & \text { H48_I1 } \\ & \text { H48_I1_MAX } \end{aligned}$ | $\begin{aligned} & \text { H49_I1 } \\ & \text { H49_I__MAX } \end{aligned}$ | $\begin{aligned} & \text { H50_I1 } \\ & \text { H50_I1_MAX } \end{aligned}$ |
| $\begin{aligned} & \mathrm{H} 2 \_\mathrm{I} 2 \\ & \mathrm{H} 2 \_12 \_\mathrm{MAX} \end{aligned}$ | $\begin{aligned} & \mathrm{H} 3 \_\mathrm{I} 2 \\ & \mathrm{H} 3 \_12 \_\mathrm{MAX} \end{aligned}$ | $\begin{aligned} & \mathrm{H} 4 \_\mathrm{I} 2 \\ & \mathrm{H} 4 \text { _I2_MAX } \end{aligned}$ | ... | $\begin{aligned} & \mathrm{H} 48 \text { _12 } \\ & \mathrm{H} 48 \_12 \_\mathrm{MAX} \end{aligned}$ | $\begin{aligned} & \mathrm{H} 49 \_12 \\ & \mathrm{H} 49 \text { _12_MAX } \end{aligned}$ | $\begin{aligned} & \text { H50_I2 } \\ & \text { H5O_I2_MAX } \end{aligned}$ |
| $\begin{array}{\|l} \hline \mathrm{H} 2 \_I 3 \\ \mathrm{H} 2 \_13 \_M A X \end{array}$ | $\begin{aligned} & \mathrm{H} 3 \text { _I3 } \\ & \text { H3_I3_MAX } \end{aligned}$ | $\begin{aligned} & \mathrm{H} 4 \_13 \\ & \mathrm{H} 4 \_13 \_\mathrm{MAX} \end{aligned}$ | ... | $\begin{array}{\|l\|} \hline \text { H48_13 } \\ \text { H48_13_MAX } \end{array}$ | $\begin{aligned} & \mathrm{H} 49 \_13 \\ & \mathrm{H} 49 \_13 \_\mathrm{MAX} \end{aligned}$ | $\begin{aligned} & \mathrm{H} 50 \_13 \\ & \mathrm{H} 50 \_13 \_\mathrm{MAX} \end{aligned}$ |

## B6 Display matrix 4-wire system, balanced load



## B7 Display matrix 4-wire systems, unbalanced load

| $\begin{aligned} & \text { U1N } \\ & \text { U2N } \\ & \text { U3N } \end{aligned}$ | $\begin{aligned} & \text { U1N_MAX } \\ & \text { U2N_MAX } \\ & \text { U3N_MAX } \end{aligned}$ | $\begin{aligned} & \text { U1N_MIN } \\ & \text { U2N_MIN } \\ & \text { U3N_MIN } \end{aligned}$ | $\begin{aligned} & \hline \text { U12 } \\ & \text { U23 } \\ & \text { U31 } \end{aligned}$ | $\begin{aligned} & \hline \text { U12_MAX } \\ & \text { U23_MAX } \\ & \text { U31_MAX } \end{aligned}$ | $\begin{aligned} & \text { U12_MIN } \\ & \text { U23_MIN } \\ & \text { U31_MIN } \end{aligned}$ | UNE <br> UNE_MAX | DEV_UMAX DEV_UMAX_MAX |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\begin{aligned} & \hline \text { UR1 } \\ & \text { UR2 } \\ & \text { UO } \\ & \hline \end{aligned}$ | $\begin{array}{\|l\|} \hline \text { UNB_UR2_UR1 } \\ \text { UNB_UR2_UR1_MAX } \end{array}$ |  |  |  |  |  |  |
| $\begin{array}{\|l} 11 \\ 12 \\ 12 \\ 13 \end{array}$ | $\begin{aligned} & \text { I1_MAX } \\ & \text { I2_MAX } \\ & \text { I__MAX } \end{aligned}$ | $\begin{array}{\|l\|} \hline \mathrm{IB} 1 \\ \mathrm{IB} 2 \\ \mathrm{IB} 3 \end{array}$ | $\begin{aligned} & \text { IB1_MAX } \\ & \text { IB2_MAX } \\ & \text { IB3_MAX } \end{aligned}$ | $\begin{aligned} & \text { IN } \\ & \text { IN_MAX } \end{aligned}$ | $\begin{aligned} & \text { DEV_IMAX } \\ & \text { DEV_IMAX_MAX } \end{aligned}$ |  |  |
| $\begin{aligned} & \mathrm{IR1} \\ & \mathrm{IR} 2 \\ & \mathrm{IO} \end{aligned}$ | $\begin{aligned} & \text { UNB_IR2_IR1 } \\ & \text { UNB_IR2_IR1_MAX } \end{aligned}$ |  |  |  |  |  |  |
| $\begin{aligned} & \mathrm{P} 1 \\ & \mathrm{P} 2 \\ & \mathrm{P} 3 \end{aligned}$ | $\begin{aligned} & \text { P1_MAX } \\ & \text { P2_MAX } \\ & \text { P3_MAX } \end{aligned}$ | $\begin{aligned} & P \\ & P \\ & \hline \text { P_MAX } \end{aligned}$ |  |  |  |  |  |
| $\begin{aligned} & \mathrm{Q} 1 \\ & \mathrm{Q} 2 \\ & \mathrm{Q} 3 \end{aligned}$ | $\begin{aligned} & \text { Q1_MAX } \\ & \text { Q2_MAX } \\ & \text { Q3_MAX } \\ & \hline \end{aligned}$ | Q |  |  |  |  |  |
| $\begin{aligned} & \text { S1 } \\ & \text { S2 } \\ & \mathrm{S} 3 \end{aligned}$ | $\begin{aligned} & \text { S1_MAX } \\ & \text { S2_MAX } \\ & \text { S3_MAX } \\ & \hline \end{aligned}$ | $\begin{aligned} & \text { S } \\ & \text { S_MAX } \end{aligned}$ |  |  |  |  |  |
| $\begin{array}{\|l} \hline \text { PF1 } \\ \text { PF2 } \\ \text { PF3 } \\ \hline \end{array}$ | $\begin{array}{\|l\|} \hline \text { PF } \\ \text { PF_MIN_IN_L } \\ \text { PF_MIN_IN_C } \\ \hline \end{array}$ | $\begin{aligned} & \hline \text { PF } \\ & \text { PF_MIN_OUT_L } \\ & \text { PF_MIN_OUT_C } \\ & \hline \end{aligned}$ | $\begin{aligned} & \text { PFG1 } \\ & \text { PFG2 } \\ & \text { PFG3 } \end{aligned}$ | $\begin{array}{\|l\|} \hline \text { PFG } \\ \text { PFG_MIN_IN_L } \\ \text { PFG_MIN_IN_C } \end{array}$ | $\begin{array}{\|l} \hline \text { PFG } \\ \text { PFG_MIN_OUT_L } \\ \text { PFG_MIN_OUT_C } \end{array}$ |  |  |
| $\begin{aligned} & \text { F_MAX } \\ & \text { F } \\ & \text { F_MIN }^{2} \end{aligned}$ |  |  |  |  |  |  |  |
| $\begin{aligned} & \mathrm{P} \\ & \mathrm{Q} \\ & \mathrm{~S} \\ & \hline \end{aligned}$ | $\begin{aligned} & \text { P } \\ & \text { U_MEAN } \\ & \text { I_MEAN } \end{aligned}$ | $\begin{array}{\|l} \hline \mathrm{P} \\ \mathrm{Q} \\ \mathrm{PF} \end{array}$ | $\begin{aligned} & \hline \mathrm{P} \\ & \mathrm{~S} \\ & \mathrm{~F} \\ & \hline \end{aligned}$ | $\begin{aligned} & \hline \mathrm{P} \\ & \mathrm{QG} \\ & \mathrm{TG} \\ & \hline \end{aligned}$ |  |  |  |
| $\begin{array}{\|l} \hline \mathrm{P} 1 \\ \mathrm{Q} 1 \\ \mathrm{~S} 1 \end{array}$ | $\begin{aligned} & \mathrm{P} 2 \\ & \mathrm{Q} 2 \\ & \mathrm{~S} 2 \end{aligned}$ | $\begin{aligned} & \hline \text { P3 } \\ & \text { Q3 } \\ & \text { S3 } \end{aligned}$ | $\begin{array}{\|l} \hline \mathrm{U} 1 \mathrm{~N} \\ \mathrm{I1} \\ \mathrm{P} 1 \\ \hline \end{array}$ | $\begin{array}{\|l} \hline \text { U2N } \\ 12 \\ \mathrm{P} 2 \\ \hline \end{array}$ | $\begin{array}{\|l} \hline \text { U3N } \\ 13 \\ \text { P3 } \\ \hline \end{array}$ |  |  |
| $\begin{array}{\|l} \hline \mathrm{D} 1 \\ \mathrm{D} 2 \\ \mathrm{D} 3 \\ \hline \end{array}$ | $\begin{aligned} & \text { D1_MAX } \\ & \text { D2_MAX } \\ & \text { D3_MAX } \end{aligned}$ | $\begin{aligned} & \hline \text { D } \\ & \text { D_MAX } \end{aligned}$ | $\begin{aligned} & \text { QG1 } \\ & \text { QG2 } \\ & \text { QG3 } \end{aligned}$ | $\begin{aligned} & \text { QG1_MAX } \\ & \text { QG2_MAX } \\ & \text { QG3_MAX } \end{aligned}$ | $\begin{aligned} & \text { QG } \\ & \text { QG_MAX } \end{aligned}$ |  |  |
| dd.mm hh.mm ss | $\begin{array}{\|l} \hline \text { OPR_CNTR1 } \\ \text { OPR_CNTR2 } \\ \text { OPR_CNTR3 } \\ \hline \end{array}$ | OPR_CNTR |  |  |  |  |  |
| $\begin{array}{\|l\|} \hline \text { THD_U1N } \\ \text { THD_U1N_MAX } \end{array}$ | $\begin{aligned} & \text { THD_U2N } \\ & \text { THD_U2N_MAX } \end{aligned}$ | $\begin{aligned} & \hline \text { THD_U3N } \\ & \text { THD_U3N_MAX } \end{aligned}$ |  |  |  |  |  |
| $\begin{aligned} & \hline \text { TDD_I1 } \\ & \text { TDD_I__MAX } \end{aligned}$ | $\begin{aligned} & \text { TDD_I2 } \\ & \text { TDD_I2_MAX } \end{aligned}$ | $\begin{aligned} & \text { TDD_I3 } \\ & \text { TDD_I3_MAX } \end{aligned}$ |  |  |  |  |  |
| Block with mean-values of power quantities |  |  |  |  |  |  |  |
| $\begin{aligned} & \text { H2_U1N } \\ & \text { H2_U1N_MAX } \end{aligned}$ | $\begin{aligned} & \text { H3_U1N } \\ & \text { H3_U1N_MAX } \end{aligned}$ | $\begin{array}{\|l} \text { H4_U1N } \\ \text { H4_U1N_MAX } \end{array}$ | ... | H48_U1N | H49_U1N H49_U1N_MAX | H50_U1N <br> H50_U1N_MAX |  |
| $\begin{aligned} & \text { H2_U2N } \\ & \text { H2_U2N_MAX } \end{aligned}$ | $\begin{aligned} & \text { H3_U2N } \\ & \text { H3_U2N_MAX } \end{aligned}$ | $\begin{aligned} & \mathrm{H} 4 \_\mathrm{U} 2 \mathrm{~N} \\ & \mathrm{H} 4 \_\mathrm{U} 2 \mathrm{~N} \_ \text {MAX } \end{aligned}$ | ... | $\begin{array}{\|l} \hline \text { H48_U2N } \\ \text { H48_U2N_MAX } \end{array}$ | $\begin{aligned} & \text { H49_U2N } \\ & \text { H49_U2N_MAX } \end{aligned}$ | $\begin{array}{\|l\|} \hline \text { H50_U2N } \\ \text { H50_U2N_MAX } \end{array}$ |  |
| $\begin{aligned} & \text { H2_U3N } \\ & \text { H2_U3N_MAX } \end{aligned}$ | $\begin{aligned} & \text { H3_U3N } \\ & \text { H3_U3N_MAX } \end{aligned}$ | $\begin{aligned} & \text { H4_U3N } \\ & \text { H4_U3N_MAX } \end{aligned}$ | $\ldots$ | H48_U3N <br> H48_U3N_MAX | H49_U3N H49_U3N_MAX | H50_U3N <br> H50_U3N_MAX |  |
| $\begin{aligned} & \text { H2_I1 } \\ & \text { H2_I1_MAX } \end{aligned}$ | $\begin{array}{\|l\|} \hline \text { H3_I_1 } \\ \text { H3_I1_MAX } \end{array}$ | $\begin{aligned} & \text { H4_I1 } \\ & \text { H4_I__MAX } \end{aligned}$ | ... | $\begin{array}{\|l} \text { H48_I1 } \\ \text { H48_I1_MAX } \end{array}$ | $\begin{array}{\|l\|} \hline \text { H49_I1 } \\ \text { H49_I__MAX } \end{array}$ | $\begin{array}{\|l\|} \hline \text { H50_I1 } \\ \text { H50_I__MAX } \end{array}$ |  |
| $\begin{aligned} & \mathrm{H} 2 \_12 \\ & \mathrm{H} 2 \_12 \_\mathrm{MAX} \end{aligned}$ | $\begin{aligned} & \text { H3_12 } \\ & \text { H3_I2_MAX } \end{aligned}$ | $\begin{aligned} & \mathrm{H} 4 \text { _I2 } \\ & \mathrm{H} 4 \_12 \_\mathrm{MAX} \end{aligned}$ | $\ldots$ | $\begin{aligned} & \text { H48_I2 } \\ & \text { H48_I2_MAX } \end{aligned}$ | $\begin{aligned} & \text { H49_I2 } \\ & \text { H49_12_MAX } \end{aligned}$ | $\begin{array}{\|l} \text { H50_I2 } \\ \text { H50_12_MAX } \end{array}$ |  |
| $\begin{aligned} & \text { H2_I3 } \\ & \text { H2_I3_MAX } \end{aligned}$ | $\begin{aligned} & \text { H3_I3 } \\ & \text { H3_I3_MAX } \end{aligned}$ | $\begin{aligned} & \text { H4_I3 } \\ & \text { H4_13_MAX } \end{aligned}$ | ... | $\begin{array}{\|l} \hline \text { H48_I3 } \\ \text { H48_I3_MAX } \end{array}$ | $\begin{aligned} & \text { H49_I3 } \\ & \text { H49_I3_MAX } \end{aligned}$ | $\begin{aligned} & \text { H50_I3 } \\ & \text { H50_13_MAX } \end{aligned}$ |  |

B8 Display matrix 4-wire system, unbalanced load, Open-Y

| U1N | U1N_MAX | U1N_MIN | U12 | U12_MAX | U12_MIN |
| :---: | :---: | :---: | :---: | :---: | :---: |
| U2N | U2N_MAX | U2N_MIN | U23 | U23_MAX | U23_MIN |
| U3N | U3N_MAX | U3N_MIN | U31 | U31_MAX | U31_MIN |
| 11 | I1_MAX | IB1 | IB1_MAX |  | DEV_IMAX |
| 12 | 12_MAX | IB2 | IB2_MAX | IN_MAX | DEV_IMAX_MAX |
| 13 | 13 MAX | IB3 | IB3_MAX |  |  |
| \|R1 | UNB_IR2_IR1 |  |  |  |  |
| IR2 | UNB_IR2_IR1_MAX |  |  |  |  |
| 10 |  |  |  |  |  |
| P1 | P1_MAX | P |  |  |  |
| P2 | P2_MAX | P_MAX |  |  |  |
| P3 | P3_MAX |  |  |  |  |
| Q1 | Q1_MAX |  |  |  |  |
| Q2 | Q2_MAX | Q_MAX |  |  |  |
| Q3 | Q3_MAX |  |  |  |  |
| S1 | S1_MAX | S |  |  |  |
| S2 | S2_MAX | S_MAX |  |  |  |
| S3 | S3_MAX |  |  |  |  |
| PF1 | PF | PF | PFG1 | PFG | PFG |
| PF2 | PF_MIN_IN_L | PF_MIN_OUT_L | PFG2 | PFG_MIN_IN_L | PFG_MIN_OUT_L |
| PF3 | PF_MIN_IN_C | PF_MIN_OUT_C | PFG3 | PFG_MIN_IN_C | PFG_MIN_OUT_C |
| F_MAX |  |  |  |  |  |
|  |  |  |  |  |  |
| F_MIN |  |  |  |  |  |
| P | P | P | P | P |  |
| Q | U_MEAN | Q | S | QG |  |
| S | I_MEAN | PF | F | TG |  |
| P1 | P2 | P3 | U1N | U2N | U3N |
| Q1 | Q2 | Q3 | 11 | 12 | 13 |
| S1 | S2 | S3 | P1 | P2 | P3 |
| D1 | D1_MAX | D | QG1 | QG1_MAX | QG |
| D2 | D2_MAX | D_MAX | QG2 | QG2_MAX | QG_MAX |
| D3 | D3_MAX |  | QG3 | QG3_MAX |  |
| dd.mm | OPR_CNTR1 | OPR_CNTR |  |  |  |
| hh.mm | OPR_CNTR2 |  |  |  |  |
| ss | OPR_CNTR3 |  |  |  |  |
| THD_U1N | THD_U2N | THD_U3N |  |  |  |
| THD_U1N_MAX | THD_U2N_MAX | THD_U3N_MAX |  |  |  |
| TDD_I1 | TDD_12 | TDD_I3 |  |  |  |
| TDD_I1_MAX | TDD_I2_MAX | TDD_I3_MAX |  |  |  |


| Block with mean-values of power quantities |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\begin{aligned} & \mathrm{H} 2 \_ \text {U1N } \\ & \text { H2_U1N_MAX } \end{aligned}$ | $\begin{aligned} & \text { H3_U1N } \\ & \text { H3_U1N_MAX } \end{aligned}$ | H4_U1N H4_U1N_MAX | ... | H48_U1N | H49_U1N H49_U1N_MAX | H50_U1N H50_U1N_MAX |
| $\begin{aligned} & \text { H2_U2N } \\ & \text { H2_U2N_MAX } \end{aligned}$ | $\begin{aligned} & \text { H3_U2N } \\ & \text { H3_U2N_MAX } \end{aligned}$ | $\begin{aligned} & \text { H4_U2N } \\ & \text { H4_U2N_MAX } \end{aligned}$ | ... | $\begin{array}{\|l} \hline \text { H48_U2N } \\ \text { H48_U2N_MAX } \end{array}$ | $\begin{aligned} & \text { H49_U2N } \\ & \text { H49_U2N_MAX } \end{aligned}$ | $\begin{aligned} & \text { H50_U2N } \\ & \text { H50_U2N_MAX } \end{aligned}$ |
| $\begin{array}{\|l} \mathrm{H} 2 \_ \text {U3N } \\ \text { H2_U3N_MAX } \end{array}$ | $\begin{aligned} & \text { H3_U3N } \\ & \text { H3_U3N_MAX } \end{aligned}$ | $\begin{aligned} & \text { H4_U3N } \\ & \text { H4_U3N_MAX } \end{aligned}$ | ... | $\begin{array}{\|l} \hline \text { H48_U3N } \\ \text { H48_U3N_MAX } \end{array}$ | $\begin{array}{\|l} \hline \text { H49_U3N } \\ \text { H49_U3N_MAX } \end{array}$ | $\begin{array}{\|l} \hline \text { H50_U3N } \\ \text { H50_U3N_MAX } \end{array}$ |
| $\begin{array}{\|l} \hline \mathrm{H} 2 \_\mathrm{II} \\ \mathrm{H} 2 \_11 \_M A X \end{array}$ | $\begin{aligned} & \mathrm{H3} \text { _I } 11 \\ & \text { H3_I1_MAX } \end{aligned}$ | $\begin{aligned} & \hline \text { H4_I1 } \\ & \text { H4_I1_MAX } \end{aligned}$ | ... | $\begin{array}{\|l\|} \hline \text { H48_I1 } \\ \text { H48_11_MAX } \end{array}$ | $\begin{aligned} & \text { H49_I1 } \\ & \text { H49_11_MAX } \end{aligned}$ | $\begin{aligned} & \text { H50_I1 } \\ & \text { H50_11_MAX } \end{aligned}$ |
| $\begin{aligned} & \mathrm{H} 2 \_\mathrm{I} 2 \\ & \mathrm{H} 2 \mathrm{I}^{\prime} 2 \text { MAX } \end{aligned}$ | $\begin{aligned} & \mathrm{H3} \text { _I2 } \\ & \mathrm{H3} \text { _I2_MAX } \end{aligned}$ | $\begin{aligned} & \text { H4_I2 } \\ & \text { H4_I2_MAX } \end{aligned}$ | $\ldots$ | $\begin{array}{\|l} \hline \text { H48_I2 } \\ \text { H48_12_MAX } \end{array}$ | $\begin{array}{\|l} \hline \text { H49_I2 } \\ \text { H49_12_MAX } \end{array}$ | $\begin{aligned} & \text { H50_12 } \\ & \text { H50_12_MAX } \end{aligned}$ |
| $\begin{aligned} & \mathrm{H} 2 \_13 \\ & \mathrm{H} 2 \_13 \_M A X \end{aligned}$ | $\begin{aligned} & \mathrm{H3} \text { _I3 } \\ & \text { H3_I3_MAX } \end{aligned}$ | $\begin{aligned} & \text { H4_I3 } \\ & \text { H4_I3_MAX } \end{aligned}$ | .. | $\begin{array}{\|l} \hline \text { H48_I3 } \\ \text { H48_I3_MAX } \end{array}$ | $\begin{array}{\|l\|} \hline \text { H49_I3 } \\ \text { H49_I3_MAX } \end{array}$ | $\begin{aligned} & \text { H50_I3 } \\ & \text { H50_13_MAX } \end{aligned}$ |

Display matrix of mean-values of power quantities

| TREND | MIN / MAX | Present | Present - 1 | Present - 2 | Present - 3 | Present - 4 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| TR_PIN | M_PIN_MAX M_PIN_MIN | M1_PIN | M2_PIN | M3_PIN | M4_PIN | M5_PIN |
| TR_POUT | M_POUT_MAX M_POUT_MIN | M1_POUT | M2_POUT | M3_POUT | M4_POUT | M5_POUT |
| TR_QIN | M_QIN_MAX M_QIN_MIN | M1_QIN | M2_QIN | M3_QIN | M4_QIN | M5_QIN |
| TR_QOUT | M_QOUT_MAX M_QOUT_MIN | M1_QOUT | M2_QOUT | M3_QOUT | M4_QOUT | M5_QOUT |
| TR_QIND | M_QIND_MAX M_QIND_MIN | M1_QIND | M2_QIND | M3_QIND | M4_QIND | M5_QIND |
| TR_QCAP | $\begin{aligned} & \text { M_QCAP_MAX } \\ & \text { M_QCAP_MIN } \end{aligned}$ | M1_QCAP | M2_QCAP | M3_QCAP | M4_QCAP | M5_QCAP |
| TR_S | $\begin{aligned} & \text { M_S_MAX } \\ & \text { M_S_MIN } \end{aligned}$ | M1_S | M2_S | M3_S | M4_S | M5_S |

## C Declaration of conformity

## C1 CE conformity

C
EG - KONFORMITÄTSERKLÄRUNG
CAMILLE BAUER EC DECLARATION OF CONFORMITY

Dokument-Nr./ Document.No.: Aplus_CE-konf.docx
Hersteller/ Manufacturer: Camille Bauer Metrawatt AG
Switzerland
Anschrift / Address: Aargauerstrasse 7
Produktbezeichnung/ Multifunktionales Leistungsmessgerät mit Netzanalyse
CH-5610 Wohlen

Product name:
ifunktionales Leistungsmessgerat mit Netzana
Multifunctional Power Monitor with System Analysis
Typ / Type:
Aplus
Das bezeichnete Produkt stimmt mit den Vorschriften folgender Europäischer Richtlinien
überein, nachgewiesen durch die Einhaltung folgender Normen:
The above mentioned product has been manufactured according to the regulations of the following European directives proven through compliance with the following standards


| Richtlinie $/$ <br> Directive | 2006/95/EG(EC) <br> Elektrische Betriebsmittel zur Verwendung innerhalb bestimmter Spannungsgrenzen - Nieder- <br> spannungsrichtlinie - CE-Kennzeichnung: 95 <br> Electrical equipment for use within certain voltage limits - Low Voltage Directive - Attachment <br> of CE marking: 95 |
| :--- | :--- |
| Norm I  <br> Standard EN $61010-1: 2010$ <br> Sicherheitsbestimmungen für elektrische Mess-, Steuer-, Regel- und Laborgeräte - Teil 1: All-  <br> gemeine Anforderungen  |  |
|  | Safety requirements for electrical equipment for measurement, control and laboratory use - <br> Part 1: General requirements <br> EN 61010-2-30: 2010 |
|  | Besondere Bestimmungen für Prüf- und Messstromkreise |
| Particular requirements for testing and measuring circuits |  |

Ort, Datum / Place, date:
Unterschrift / signature:
M. Ulrich
M. Lid

Leiter Technik / Head of engineering

Wohlen, 01. September 2014


## C2 FCC statement

The following statement applies to the products covered in this manual, unless otherwise specified herein. The statement for other products will appear in the accompanying documentation.

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to Part 15 of the FCC Rules and meets all requirements of the Canadian Interference-Causing Equipment Standard ICES-003 for digital apparatus. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/T.V. technician for help.

Camille Bauer AG is not responsible for any radio television interference caused by unauthorized modifications of this equipment or the substitution or attachment of connecting cables and equipment other than those specified by Camille Bauer AG. The correction of interference caused by such unauthorized modification, substitution or attachment will be the responsibility of the user.

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